

10

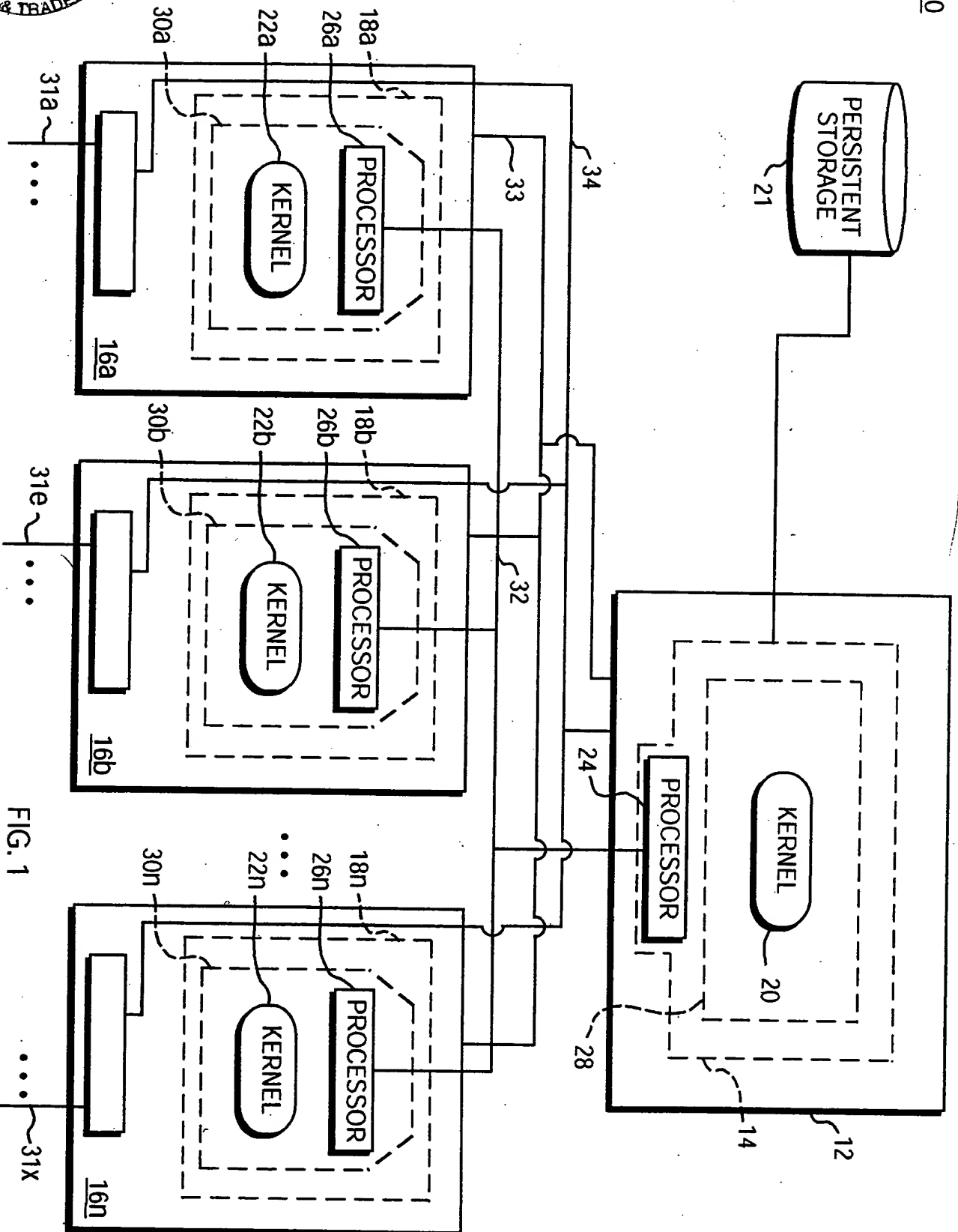


FIG. 1

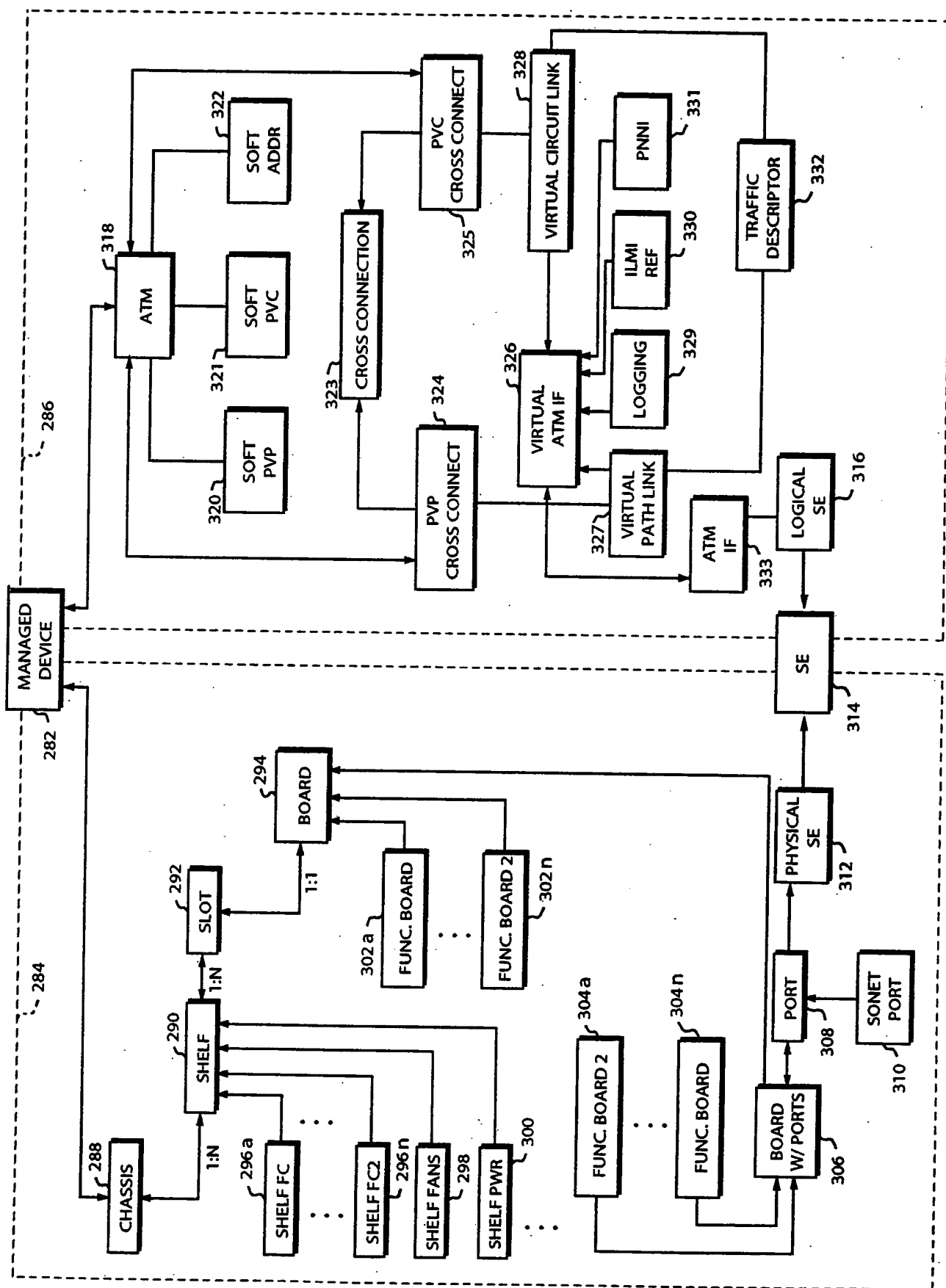


FIG. 2

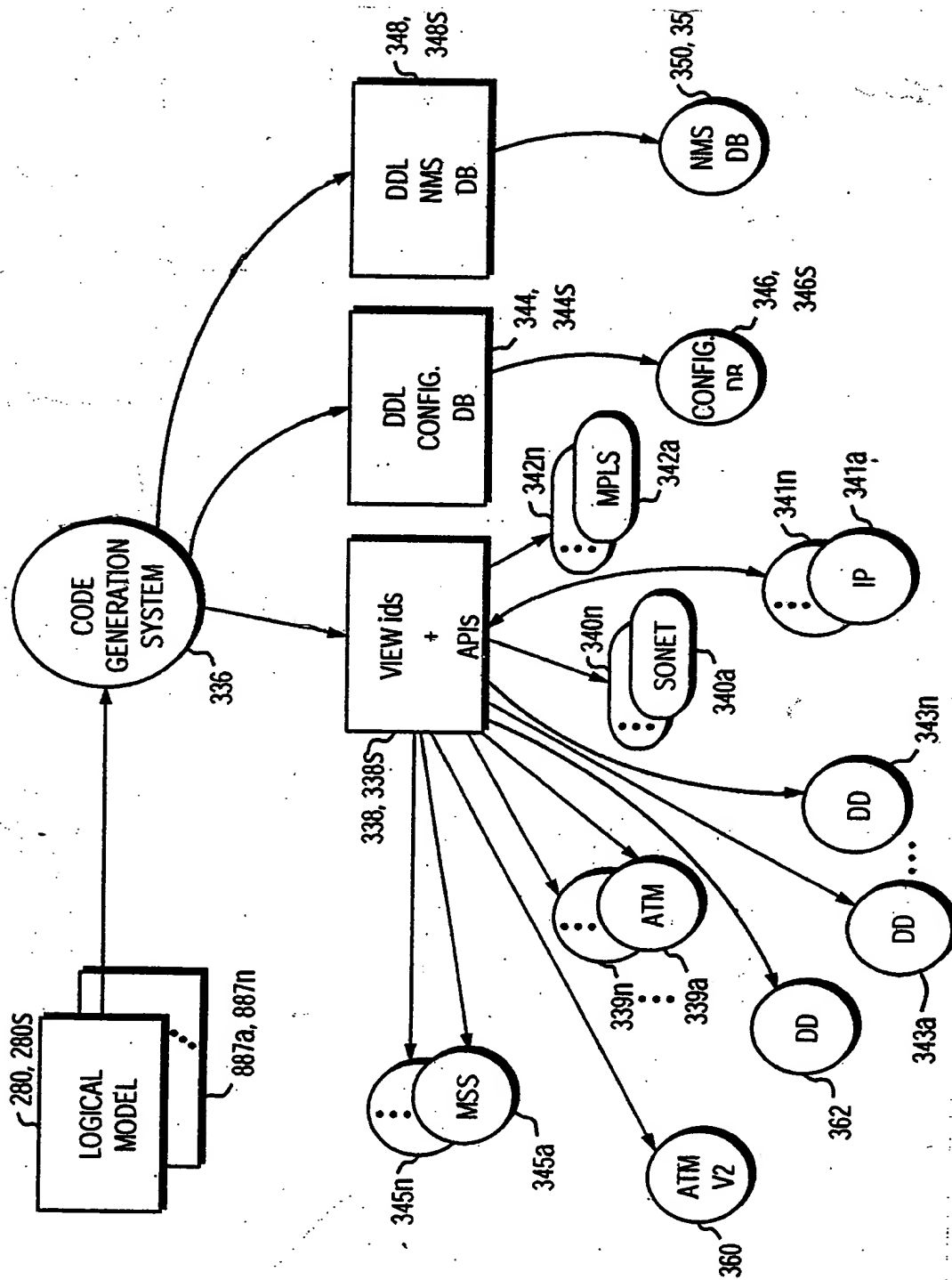


FIG. 3

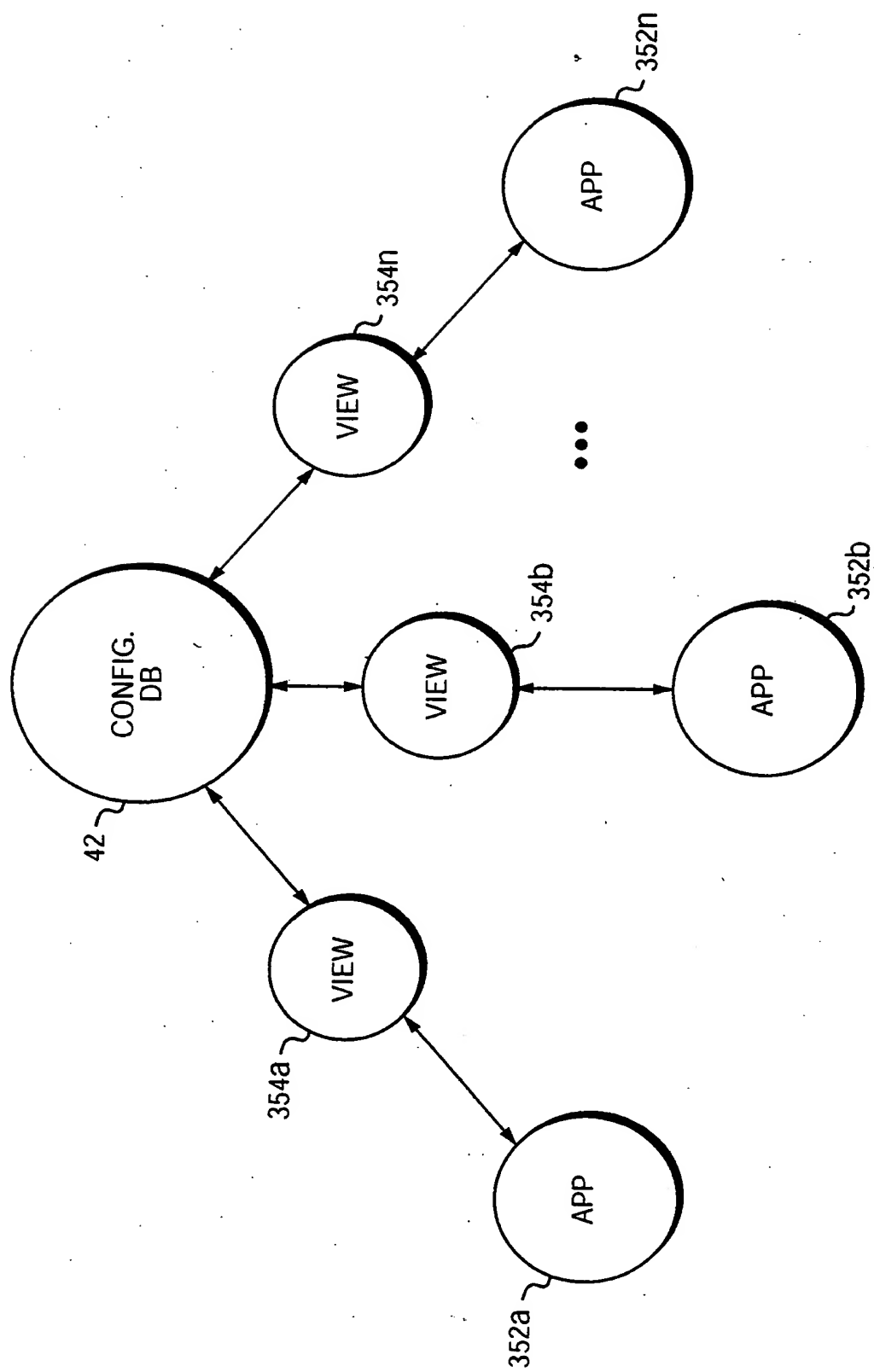


FIG. 4

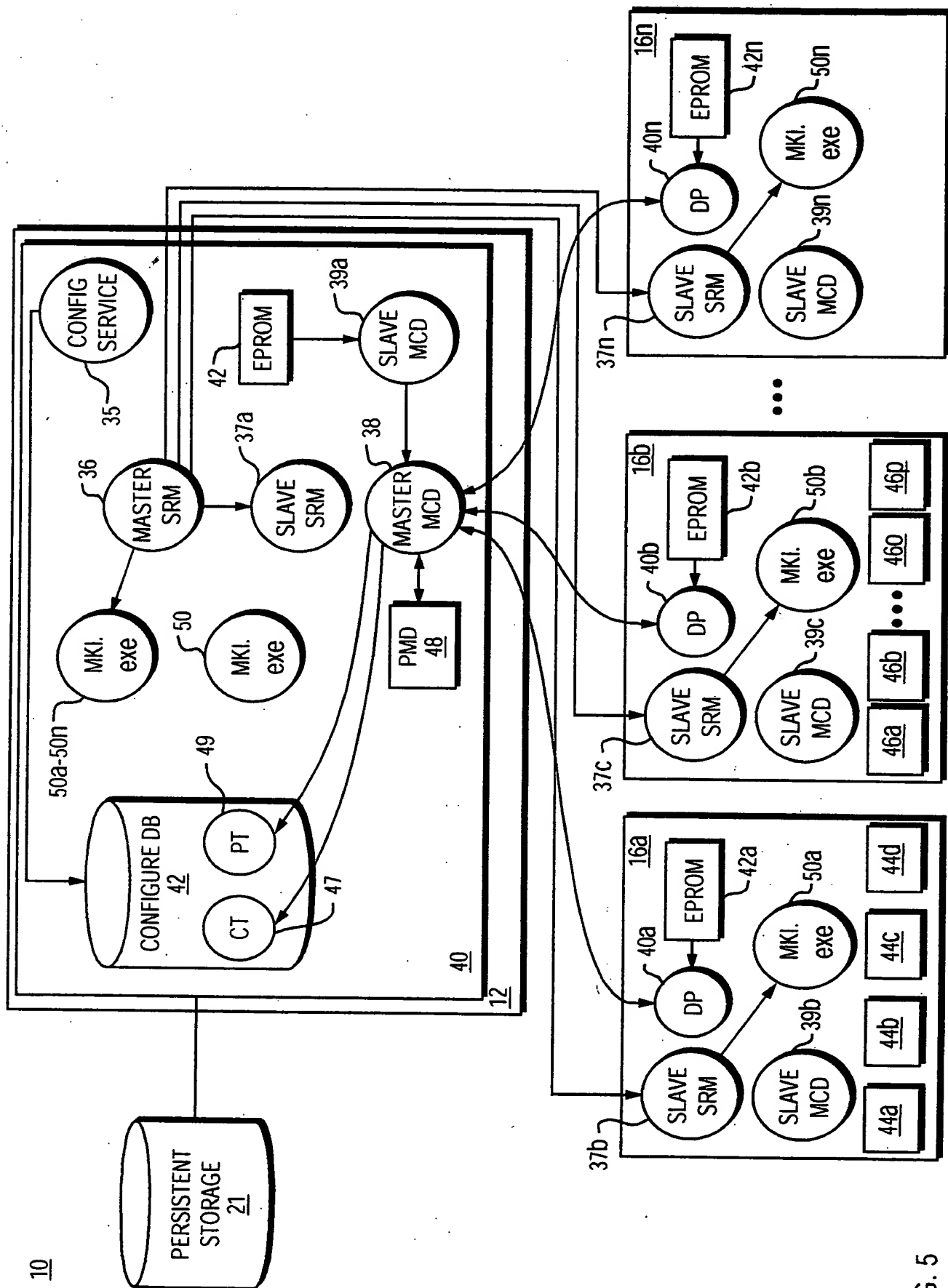


FIG. 5

CARD TABLE 47

	PID	CWD TYPE	VERSION NO.	SLOT NO.	...
16a	500	0XF002	3	1	
16b	501	0XF002	4	2	
	⋮	⋮	⋮	⋮	⋮
16e	505	0X6002	1	5	
	⋮	⋮	⋮	⋮	⋮
16n	513	0XF002	1	12	
	⋮	⋮	⋮	⋮	⋮

FIG. 6

PORT TABLE 49

	PID	PORT TYPE	VERSION NO.	SLOT NO.	...
44a	1500	00620	1	1	
44b	1501	00620	1	1	
44c	1502	00620	1	1	
44d	1503	00620	1	1	
44a	1504	00820			
46a	⋮	⋮	⋮	⋮	⋮
	1600	00620	1	8	
	⋮	⋮	⋮	⋮	⋮

FIG. 7

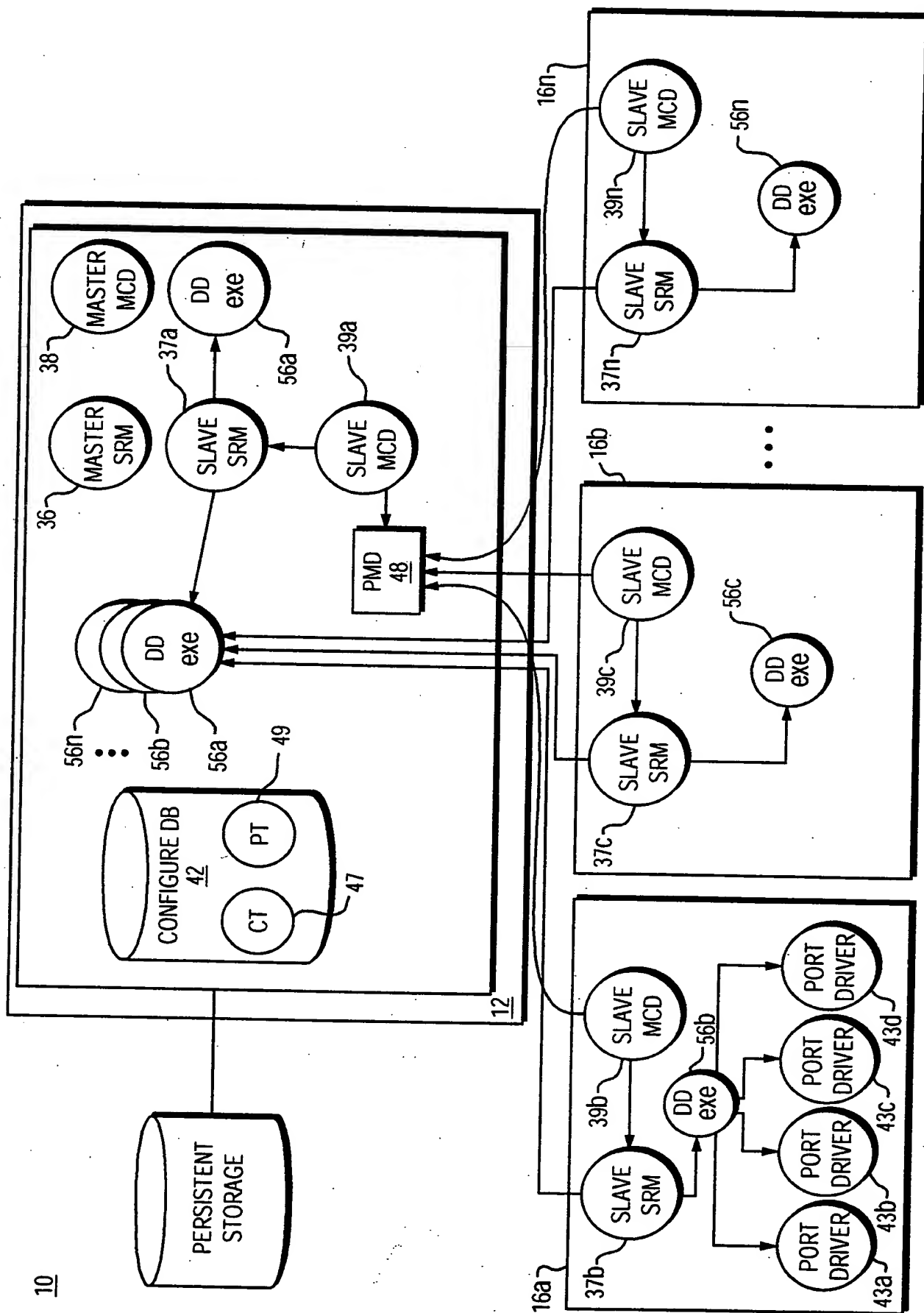


FIG. 8

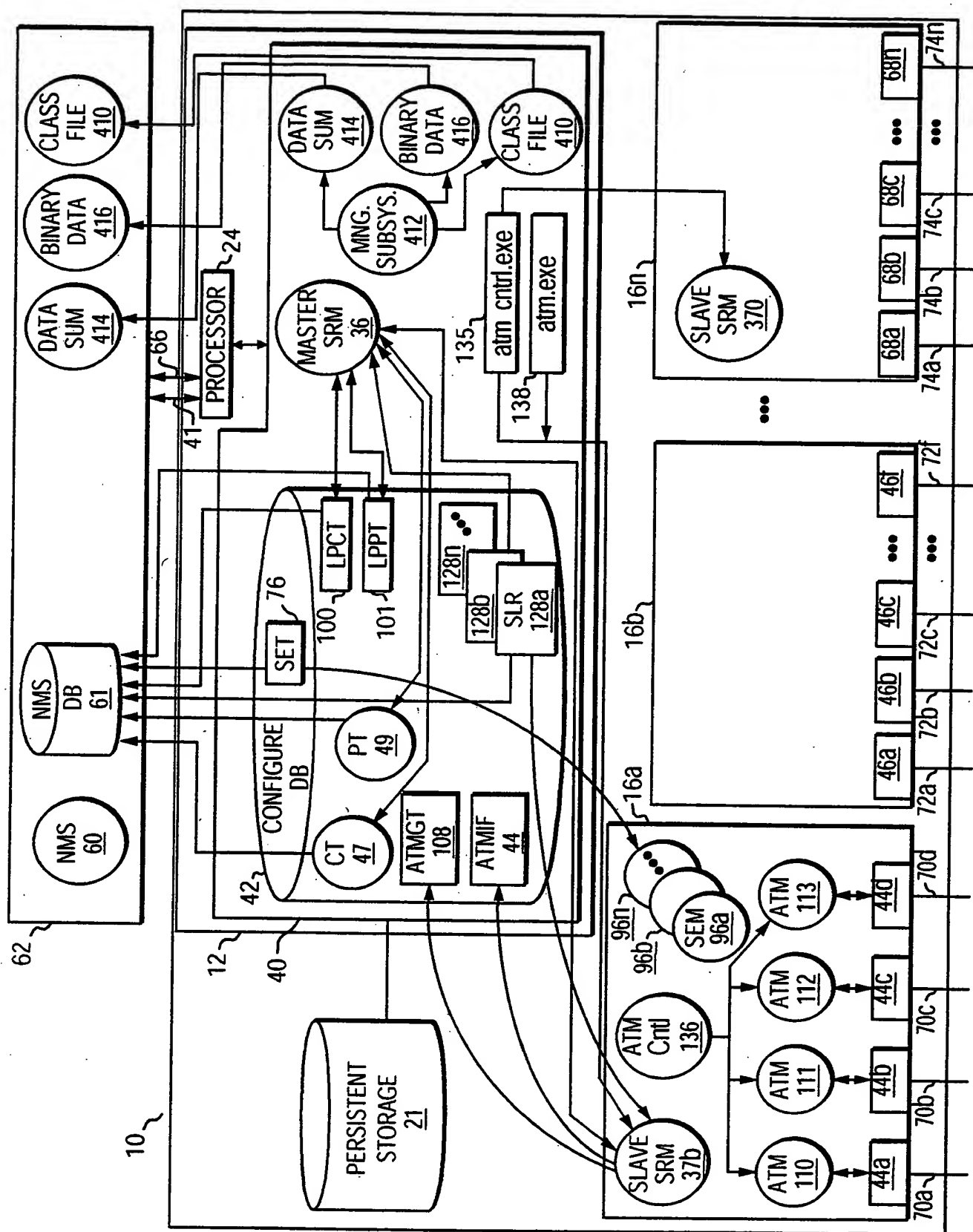


FIG. 9

SERVICE ENDPOINT TABLE 76

	SERVICE ENDPOINT #	PORT PID
78	1	1500
80	2	1501
82	3	1501
84	4	1501
86	5	1502
88	6	1502
90	7	1503
92	8	1503
94	9	1503
168	10	1502
	⋮	⋮

FIG. 10

LOGICAL TO PHYSICAL CARD TABLE 100

	98 LID	102 PRIMARY PID	104 BACK-UP PID
106	30	500	513
109	31	501	513
	⋮	⋮	⋮

FIG. 11A

LOGICAL TO PHYSICAL PORT TABLE 101

	98 LID	102 PRIMARY PID	104 BACK-UP PID
107	40	1500	1600
	⋮	⋮	⋮

FIG. 11B

ATM GROUP TABLE 108

GROUP #	CARD LID	...
1	30	
2	30	
3	30	
4	30	

FIG. 12

ATM INTERFACE TABLE 114

ATM IF	ATM GROUP	SE	...
1	1	1	
2	1	1	
3	1	1	
4	2	2	
5	2	3	
6	2	4	
⋮	⋮	⋮	⋮
12	3	10	
⋮	⋮	⋮	⋮

FIG. 13

SOFTWARE LOAD RECORD 128a

130	CONTROL SHIM	LID	132
134	atm-cntl.exe	30	

FIG. 14

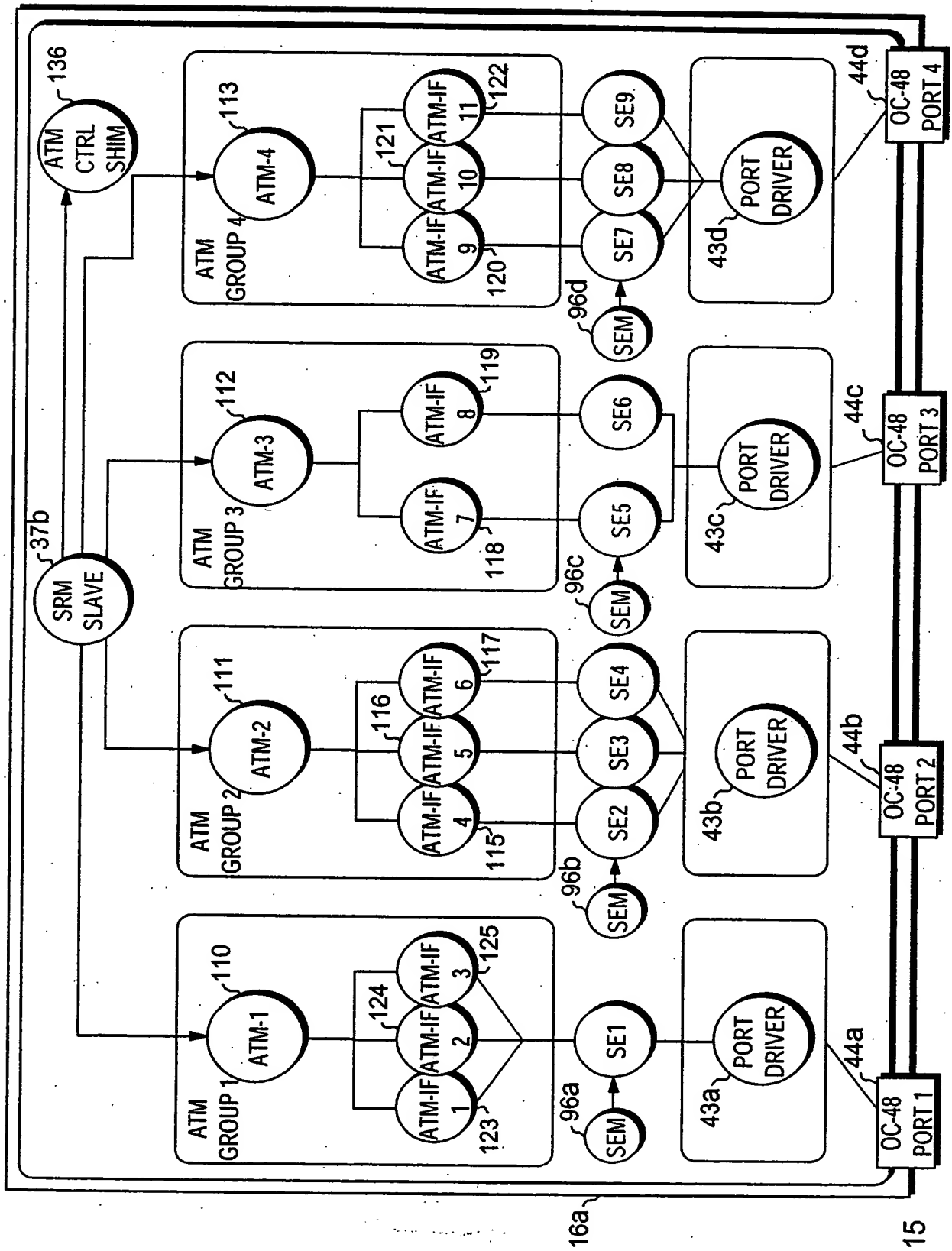


FIG. 15

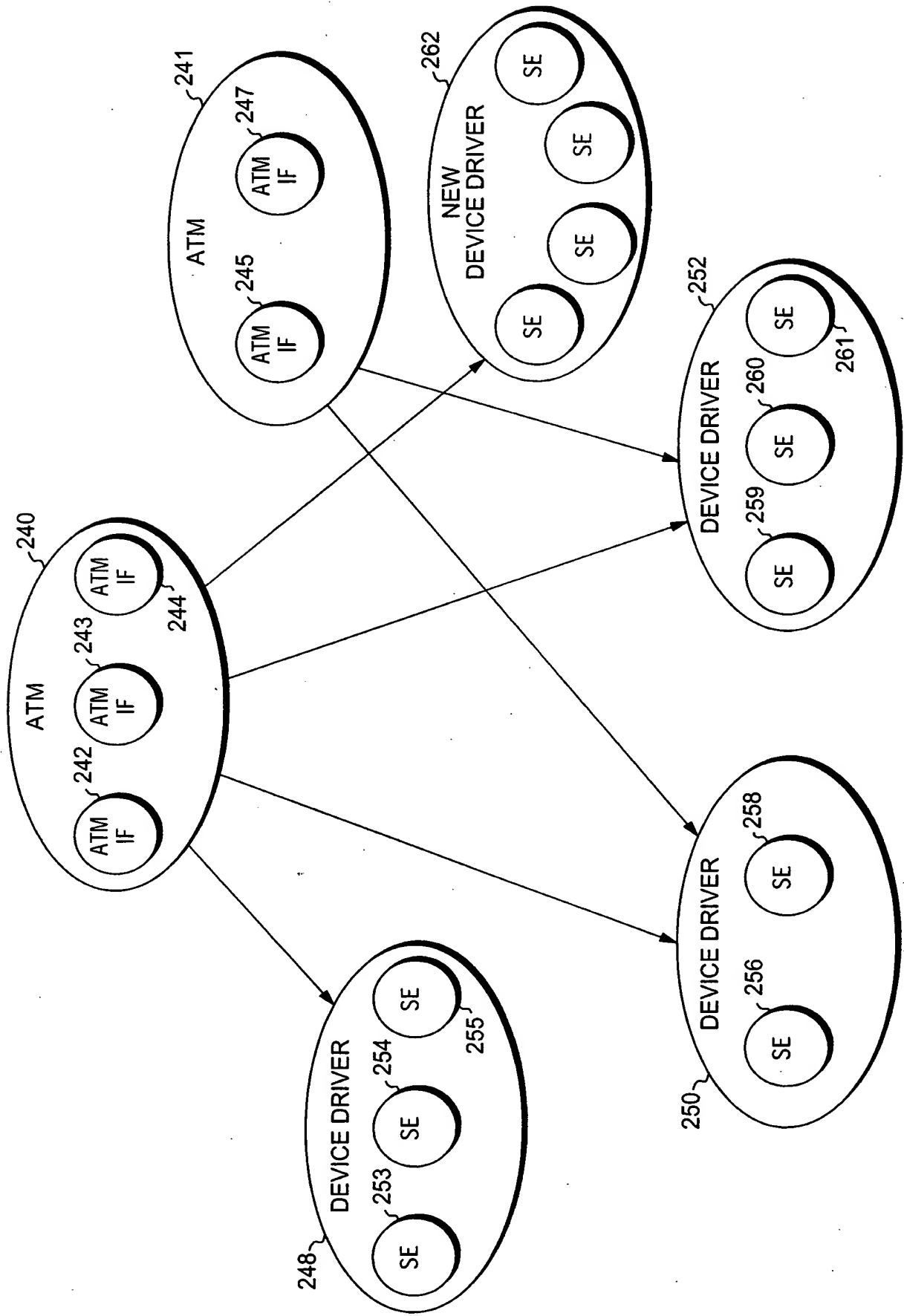


FIG. 16A

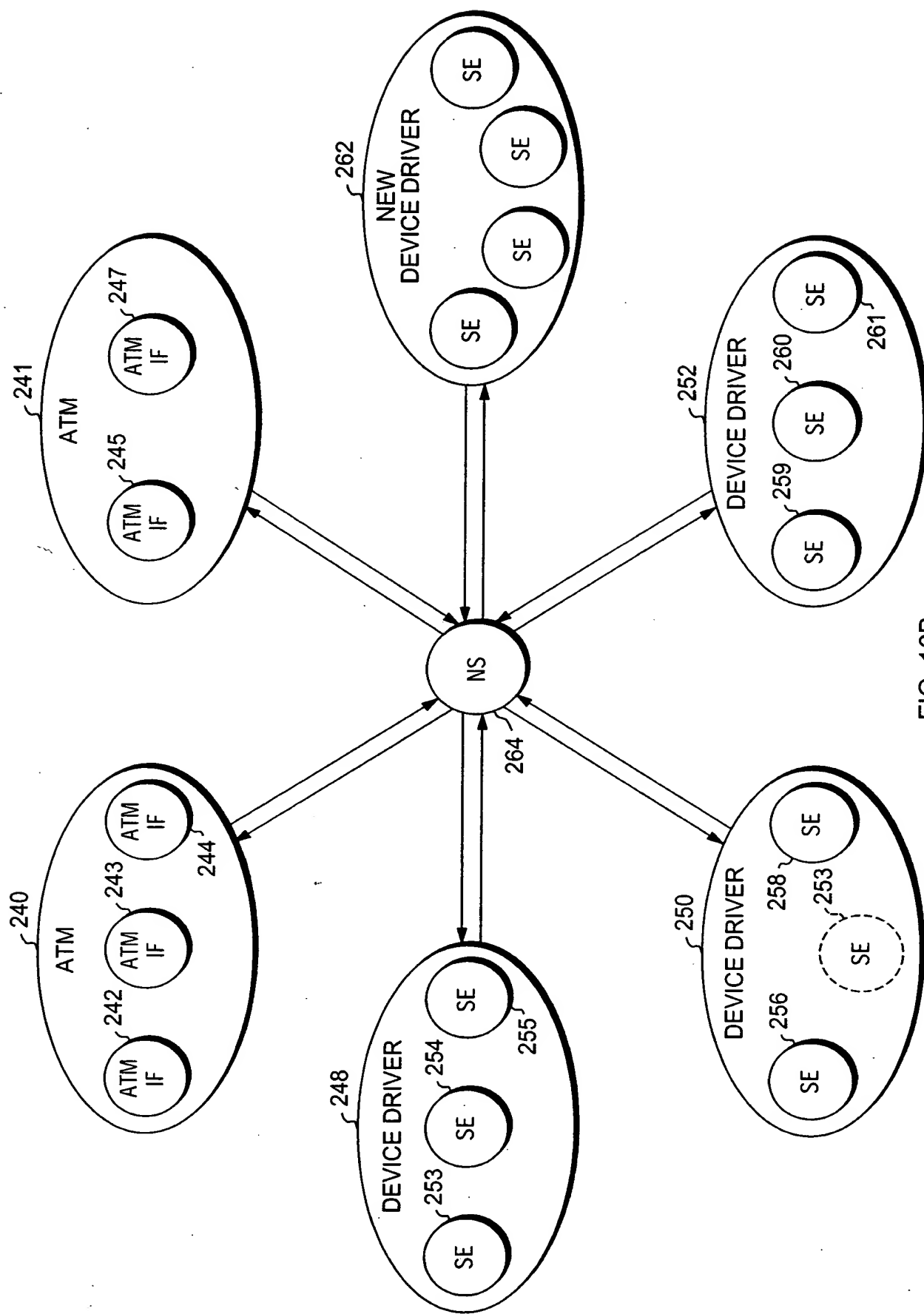


FIG. 16B

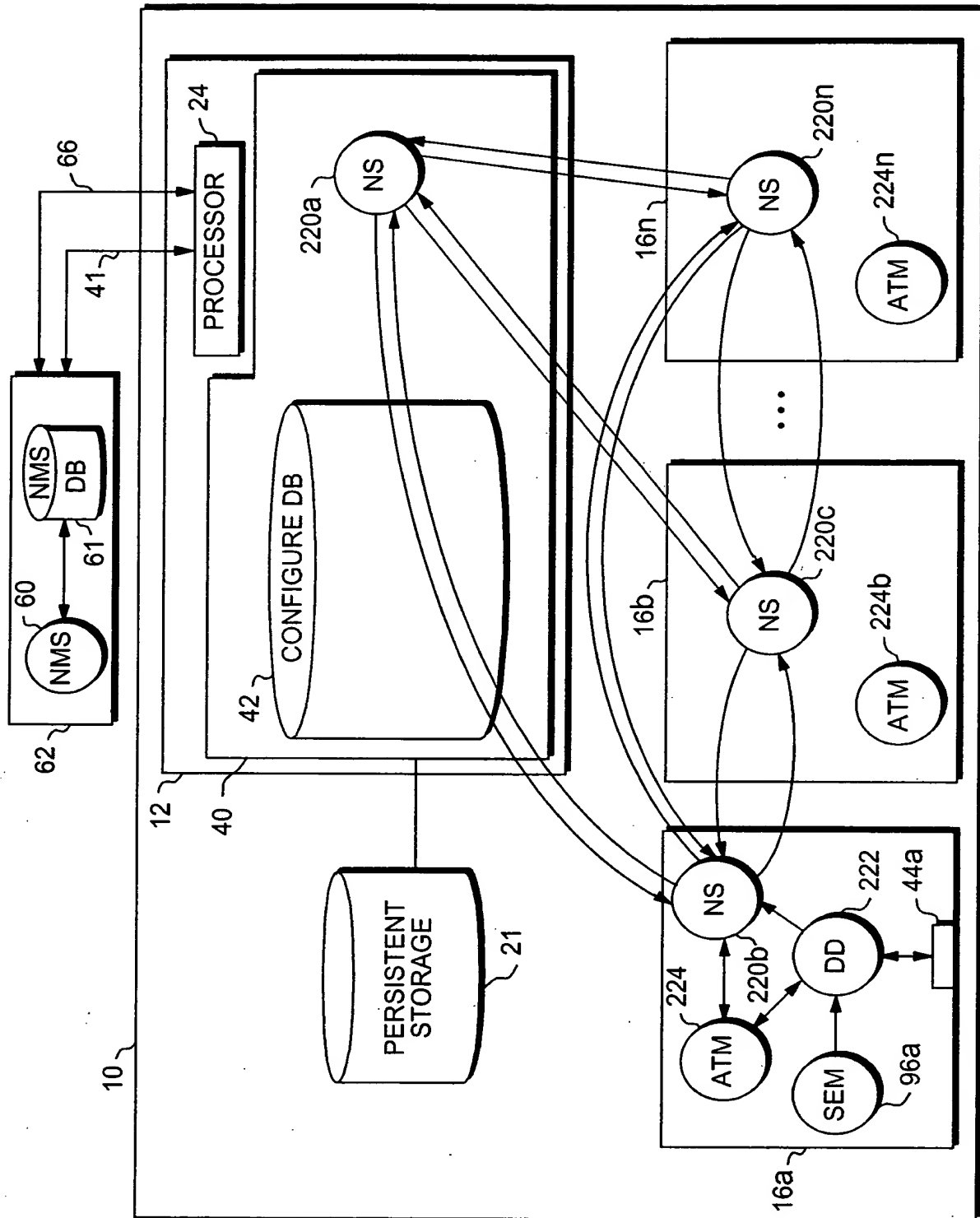


FIG. 16C

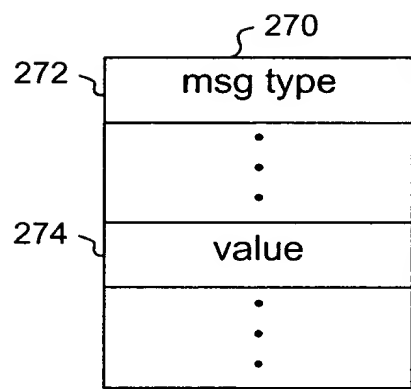


FIG. 16D

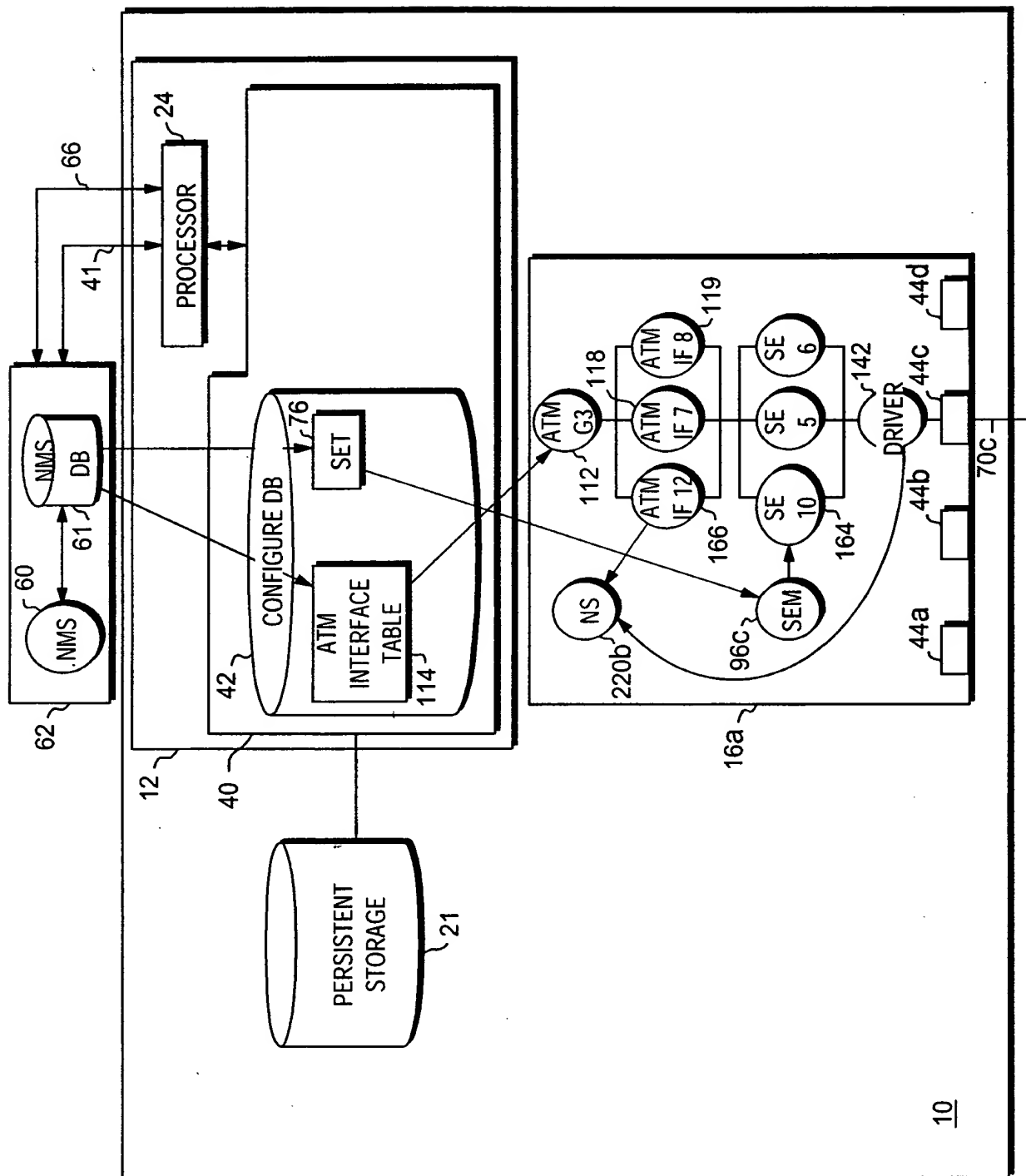


FIG. 17

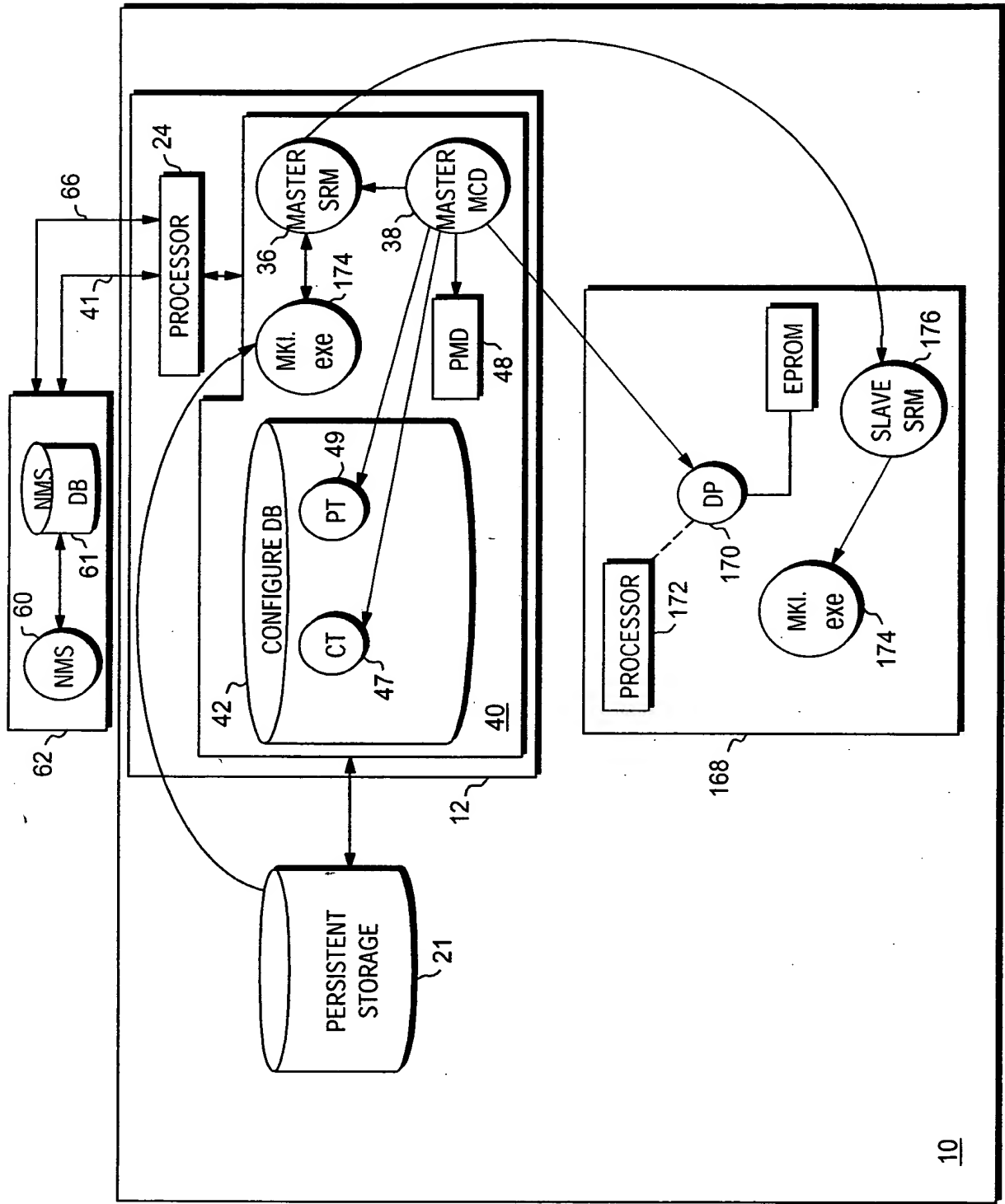


FIG. 18

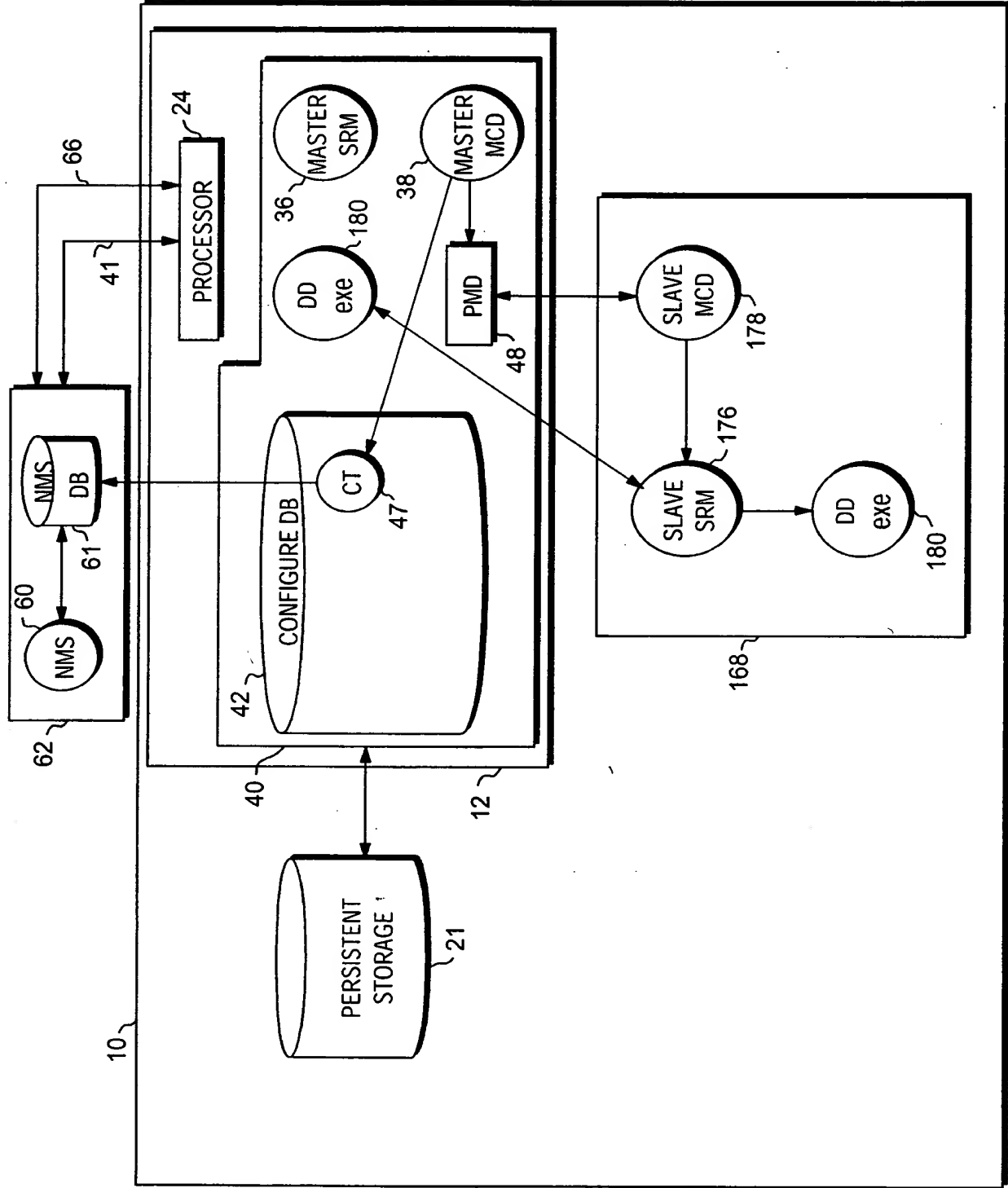


FIG. 19

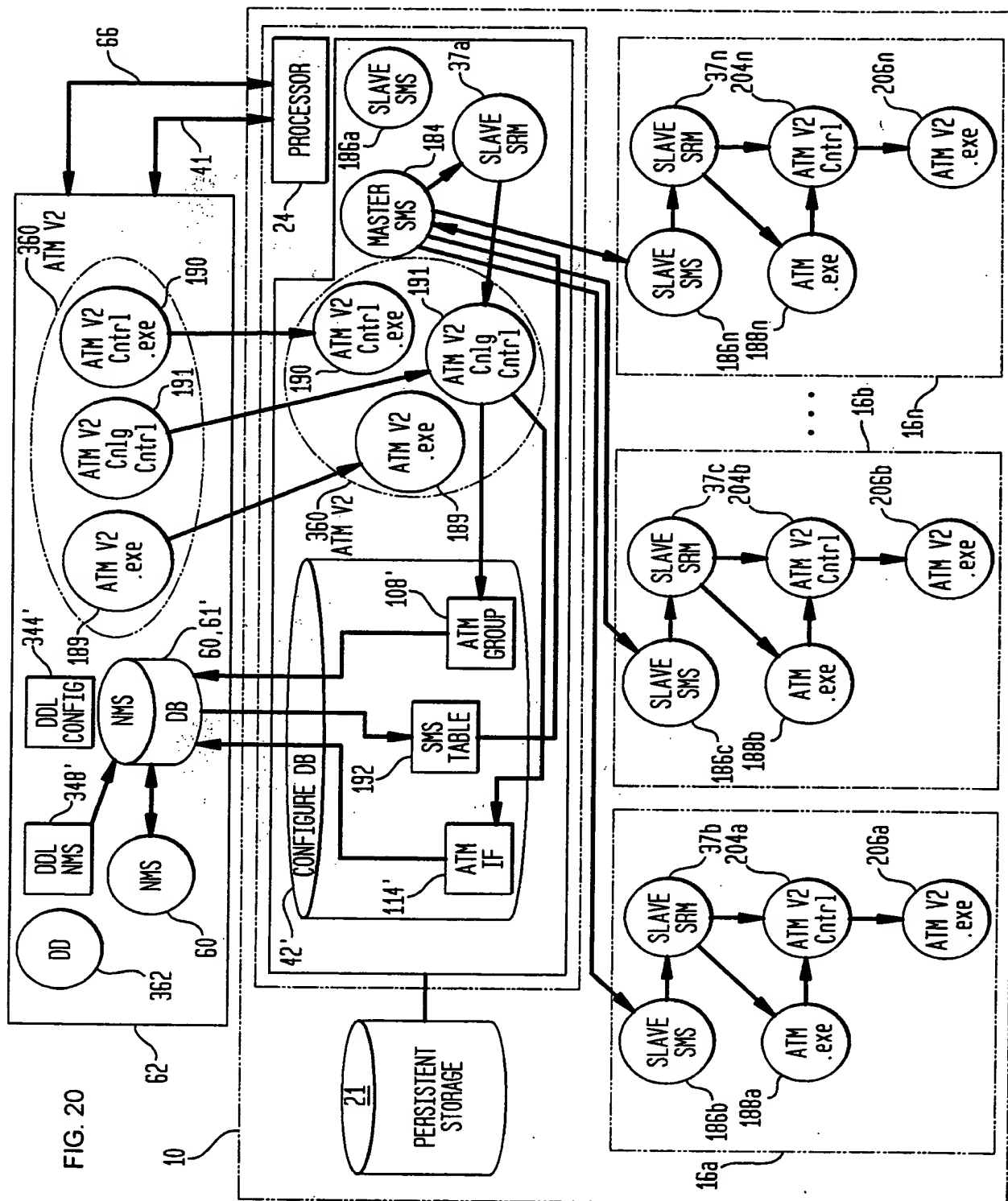


FIG. 21

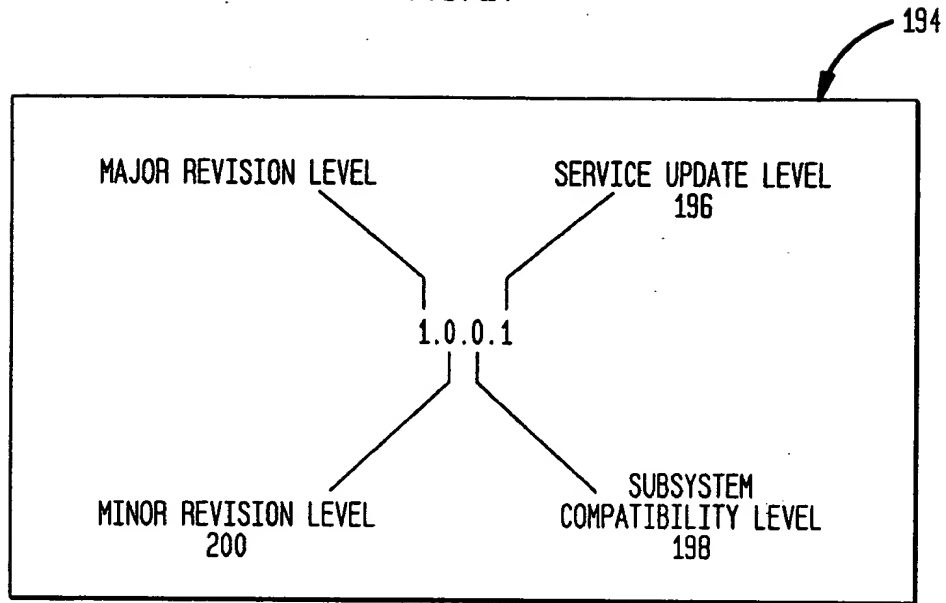


Figure 1 is a block diagram illustrating a network architecture. The architecture is divided into three main sections: a central system (10), a peripheral system (12), and another peripheral system (13).

Central System (10):

- Contains an **NMS** (Network Management System) labeled 348'.
- Contains an **NMS DB** (Network Management System Database) labeled 61'.
- Contains a **DDL CONFIG. DB** (Data Definition Language Configuration Database) labeled 344'.
- Contains an **ATM V2** (Asynchronous Transfer Mode Version 2) component labeled 360.
- The **NMS** (348') is connected to the **NMS DB** (61') and the **DDL CONFIG. DB** (344').
- The **ATM V2** (360) is connected to the **DDL CONFIG. DB** (344').

Peripheral System (12):

- Contains a **Config. DB** (Configuration Database) labeled 192.
- Contains an **SMS TABLE** (Service Management System Table) labeled 360.
- Contains a **MASTER SMS** (Master Service Management System) labeled 360.
- Contains an **ATM V2** (Asynchronous Transfer Mode Version 2) component labeled 360.
- The **Config. DB** (192) is connected to the **SMS TABLE** (360).
- The **MASTER SMS** (360) is connected to the **SMS TABLE** (360).
- The **ATM V2** (360) is connected to the **SMS TABLE** (360).

Peripheral System (13):

- Contains a **Config. DB** (Configuration Database) labeled 192'.
- Contains an **SMS TABLE** (Service Management System Table) labeled 420.
- Contains a **SLAVE SMS** (Slave Service Management System) labeled 186e.
- Contains a **SLAVE SPM** (Slave Service Management System Process) labeled 37e.
- The **Config. DB** (192') is connected to the **SMS TABLE** (420).
- The **SLAVE SMS** (186e) is connected to the **SMS TABLE** (420).
- The **SLAVE SPM** (37e) is connected to the **SLAVE SMS** (186e).

Connections:

- The **DDL CONFIG. DB** (344') in the central system (10) is connected to the **Config. DB** (192) in the peripheral system (12).
- The **DDL CONFIG. DB** (344') in the central system (10) is connected to the **Config. DB** (192') in the peripheral system (13).
- The **ATM V2** (360) in the central system (10) is connected to the **ATM V2** (360) in the peripheral system (12).
- The **ATM V2** (360) in the central system (10) is connected to the **ATM V2** (360) in the peripheral system (13).

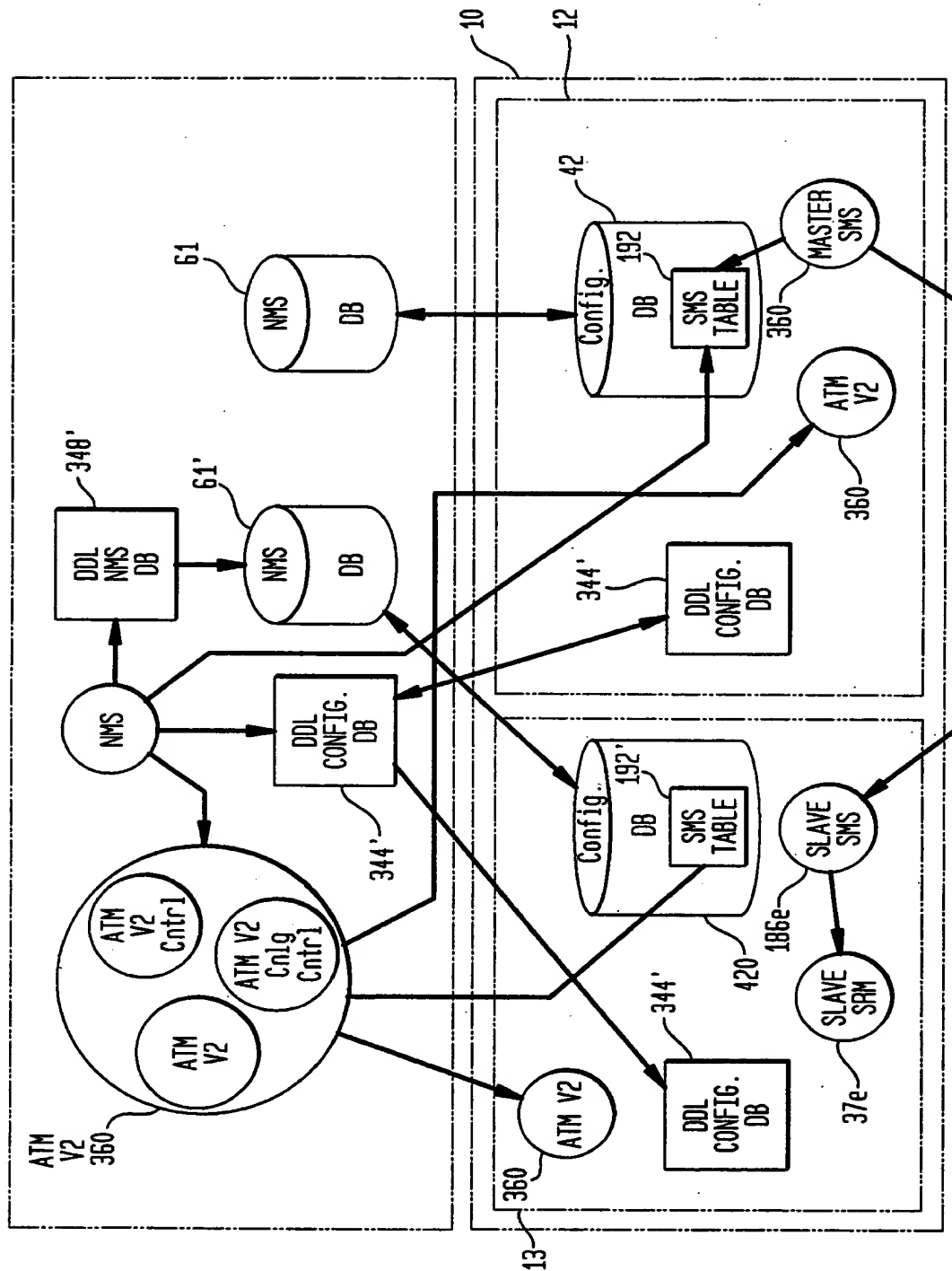
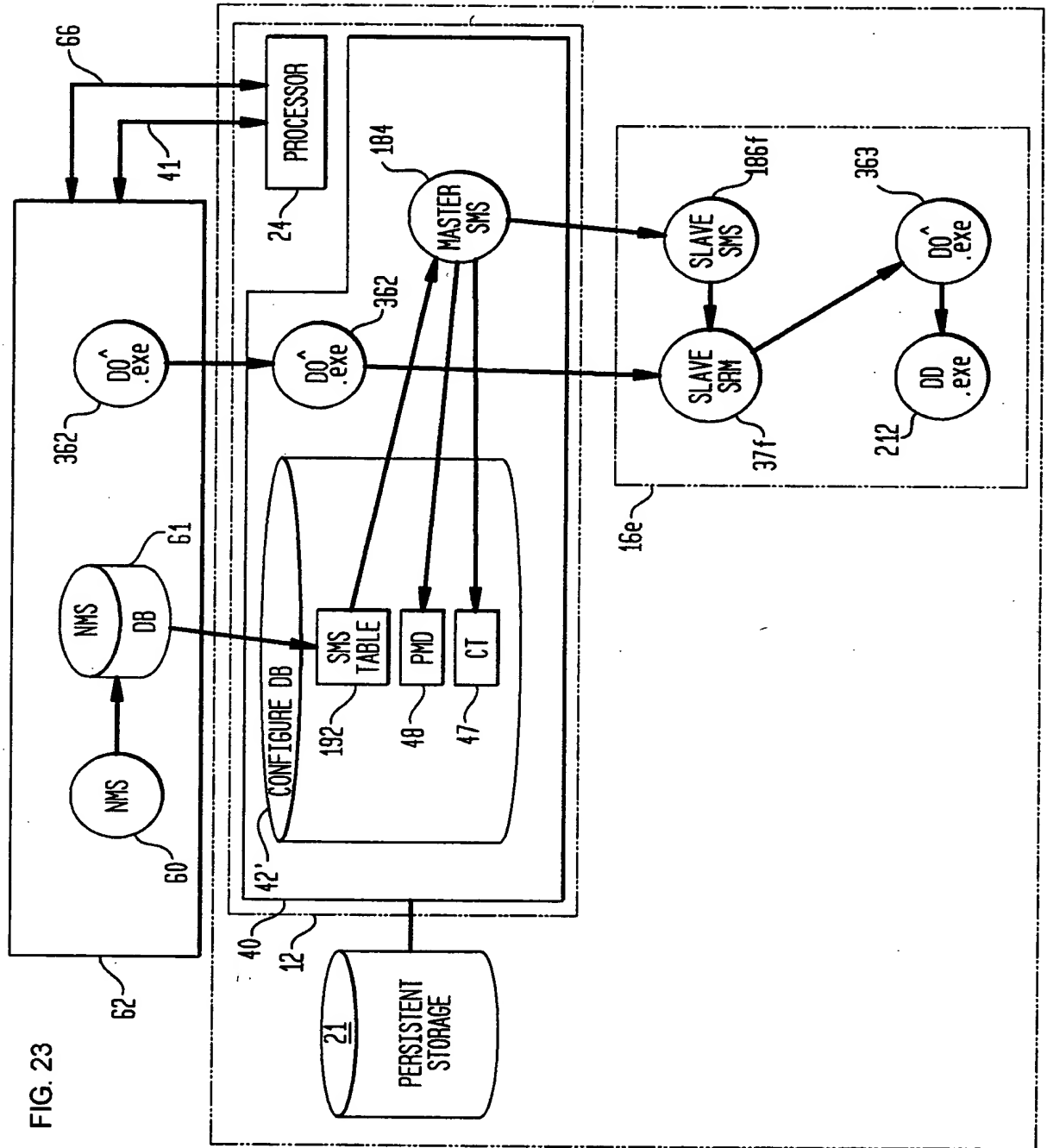


FIG. 23



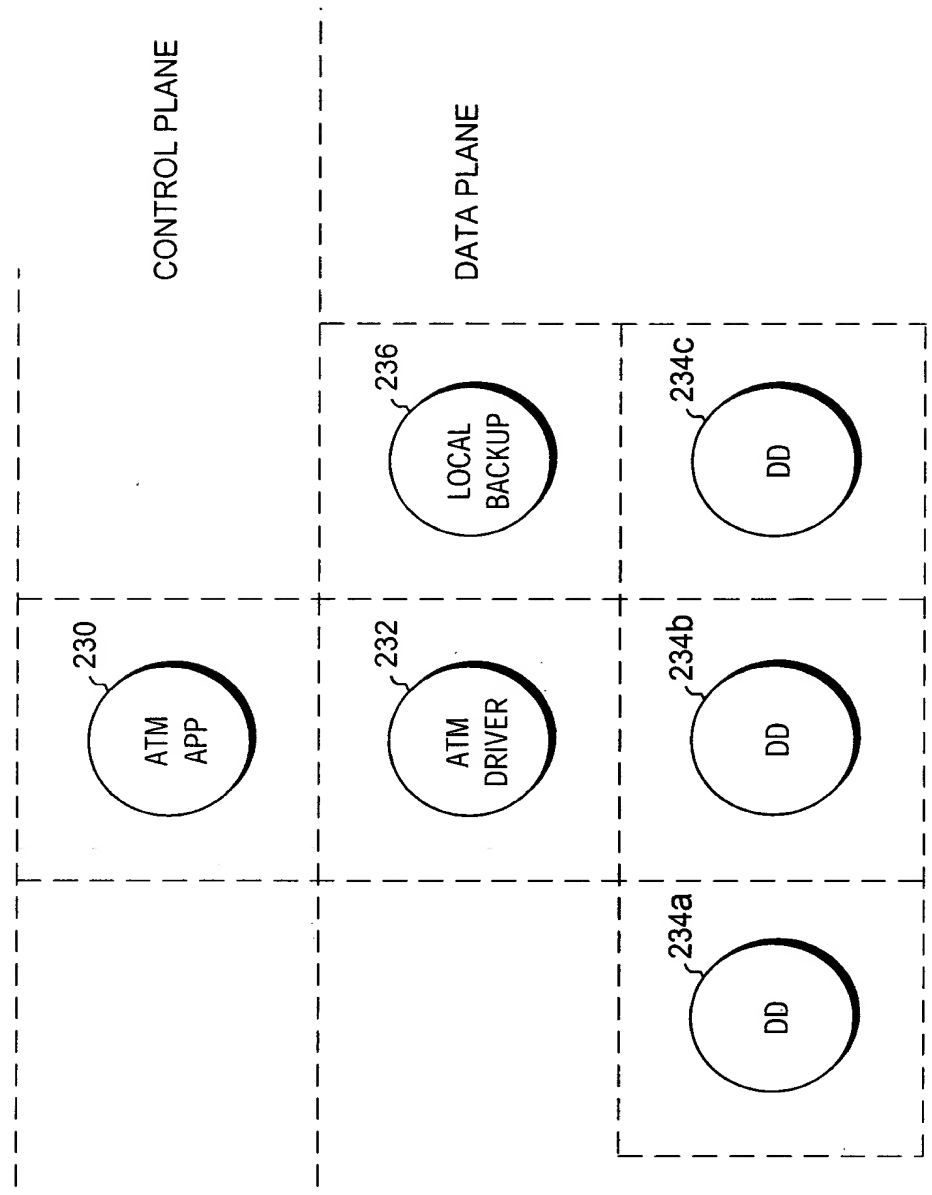


FIG. 24

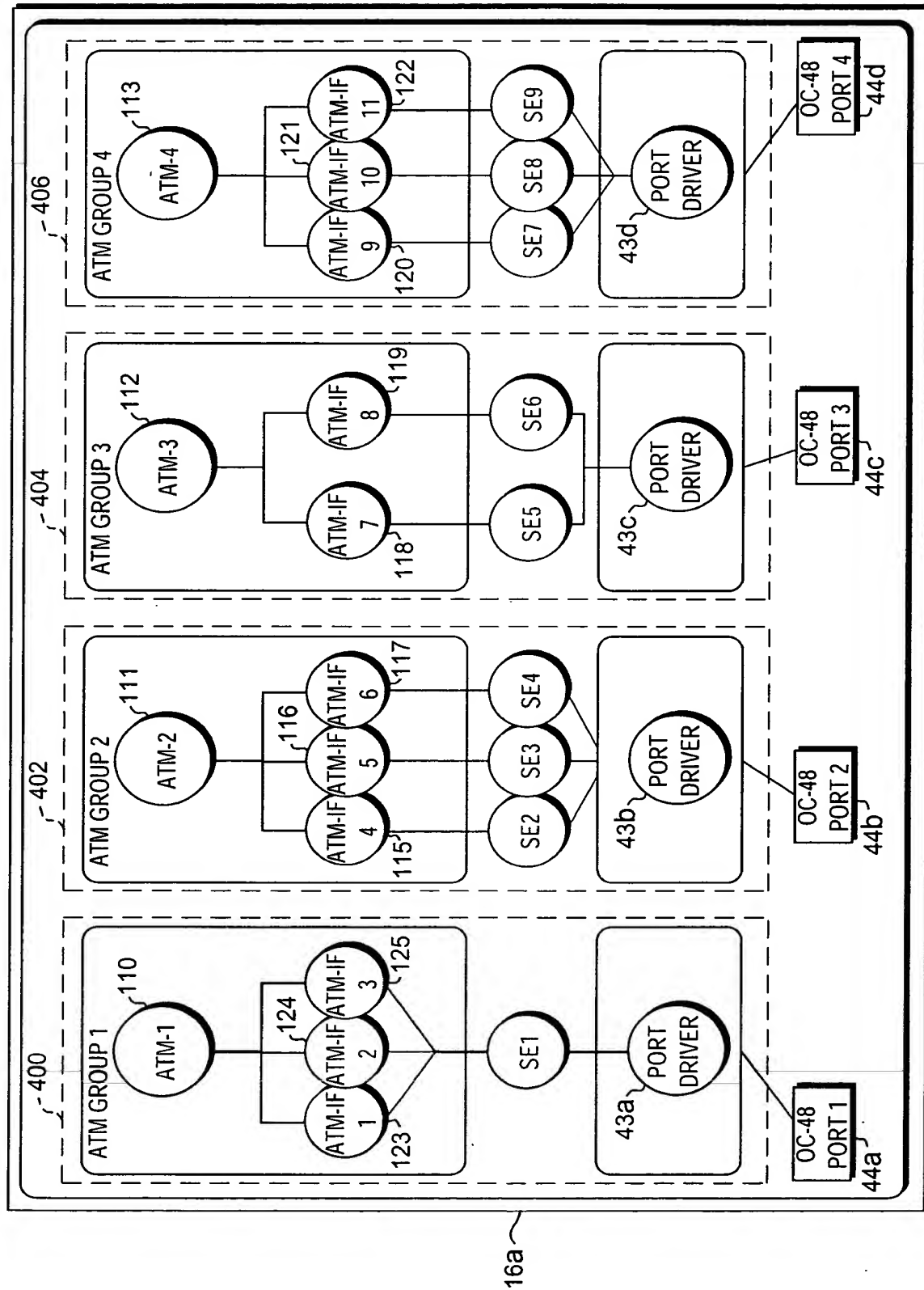


FIG. 25

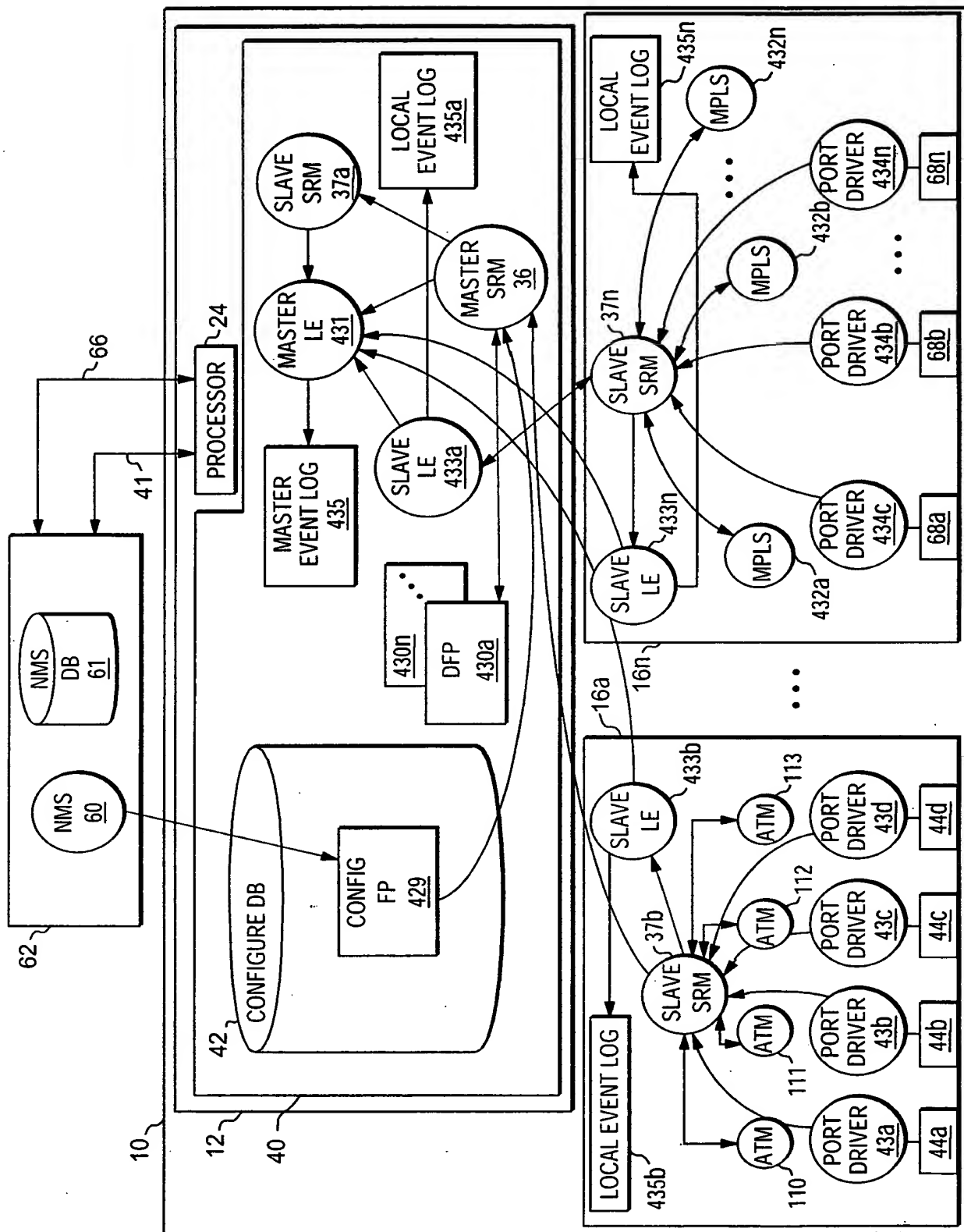


FIG. 26

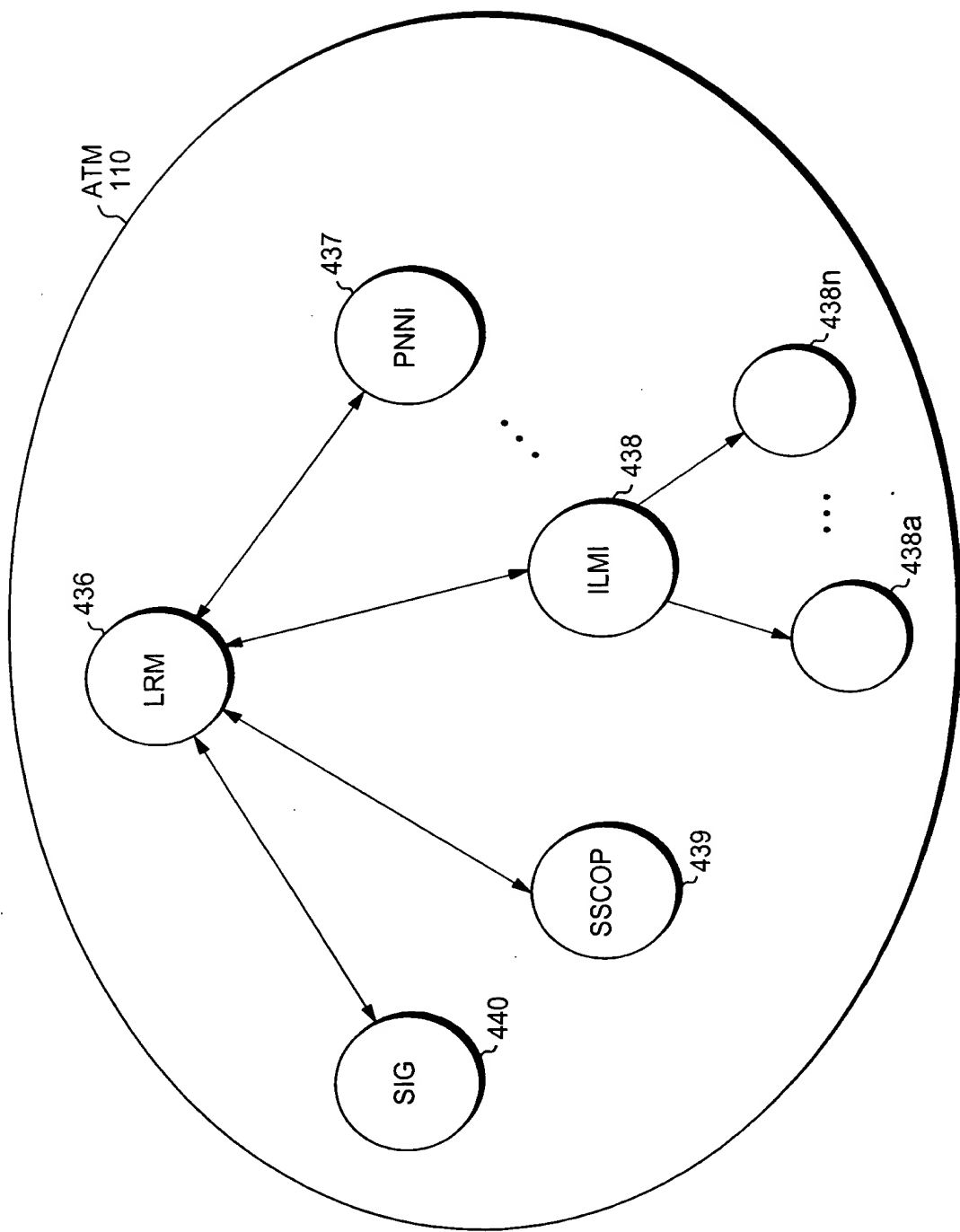


FIG. 27

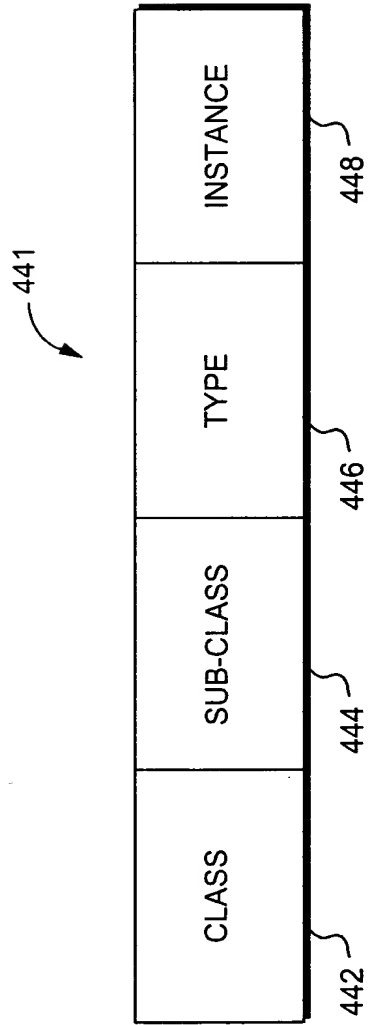


FIG. 28

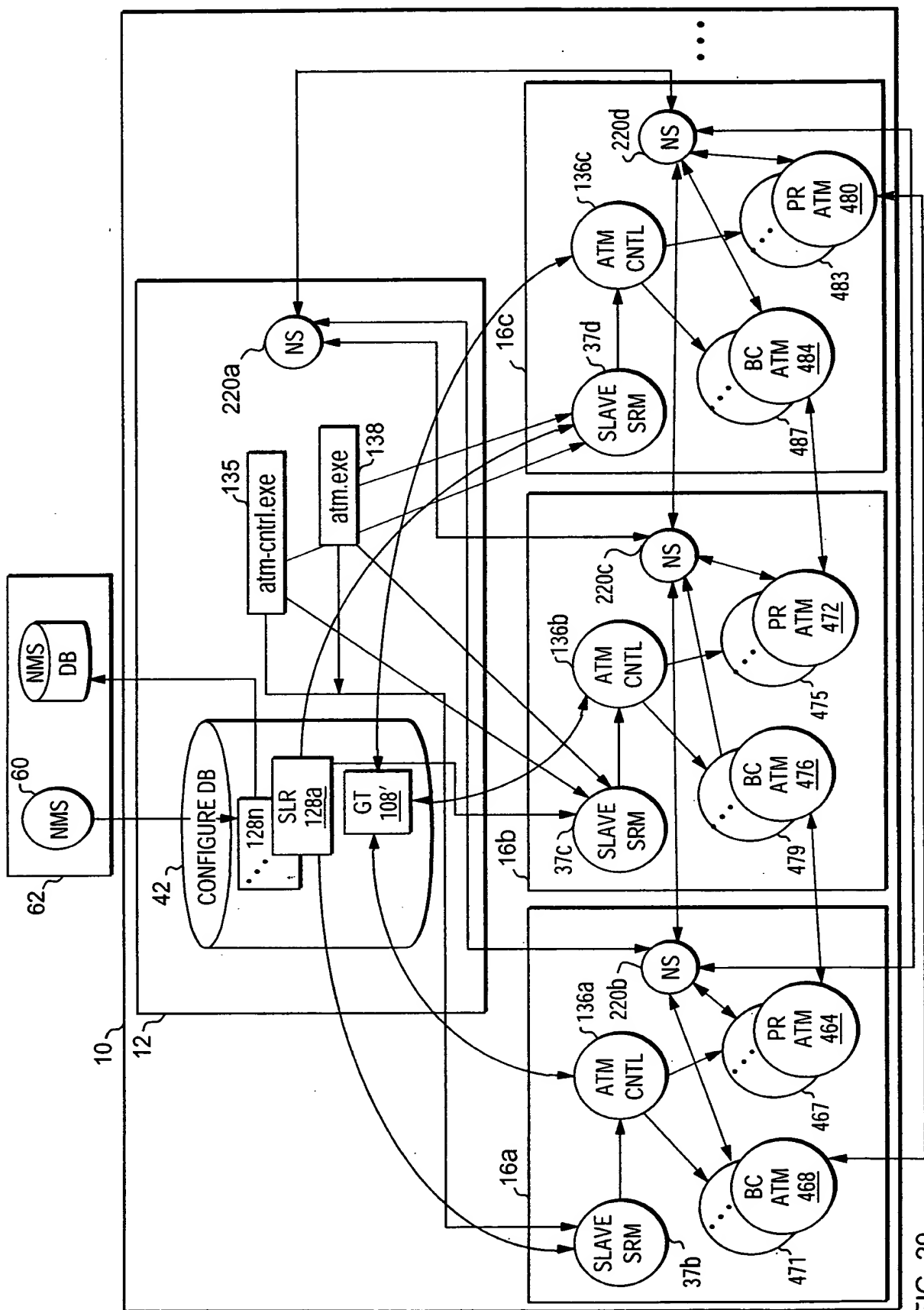


FIG. 29

GROUP TABLE 108'

		447	449	
	GROUP #	PRIMARY CARD LID	BACKUP CARD LID	...
450	1	30	31	
451	2	30	31	
452	3	30	31	
453	4	30	31	
454	5	31	32	
455	6	31	32	
456	7	31	32	
457	8	31	32	
458	9	32	30	
459	10	32	30	
460	11	32	30	
461	12	32	30	
	• • •	• • •	• • •	• • •

FIG. 30

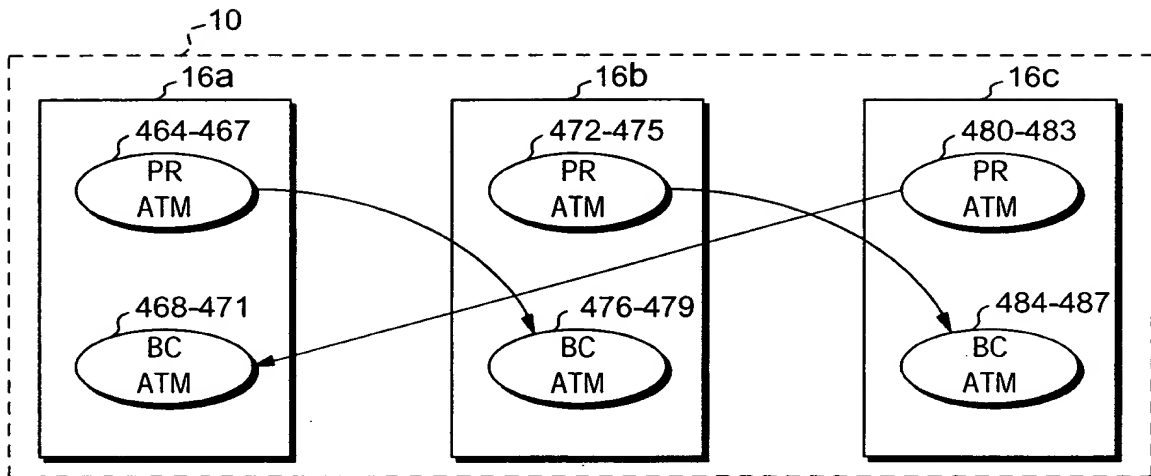


FIG. 31A

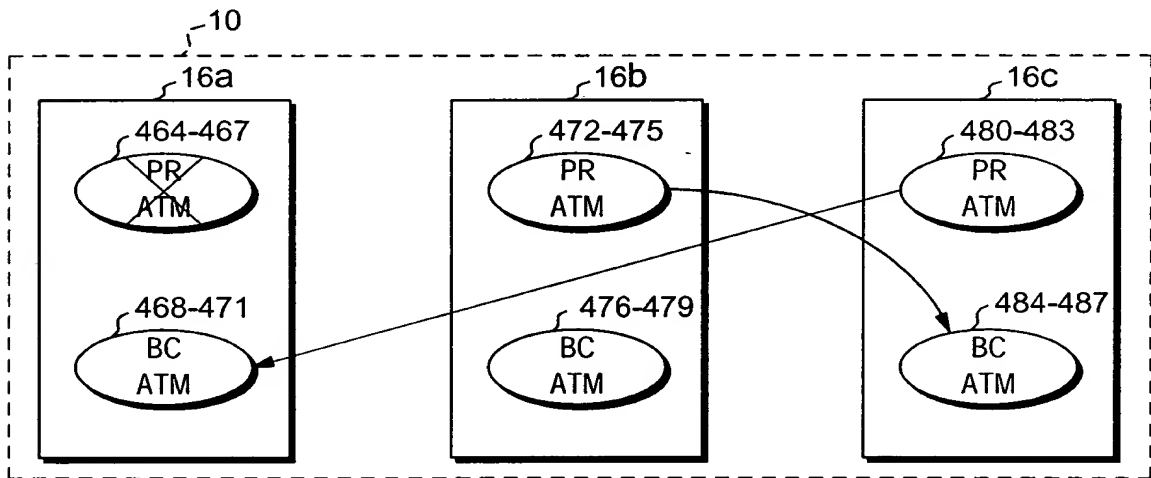


FIG. 31B

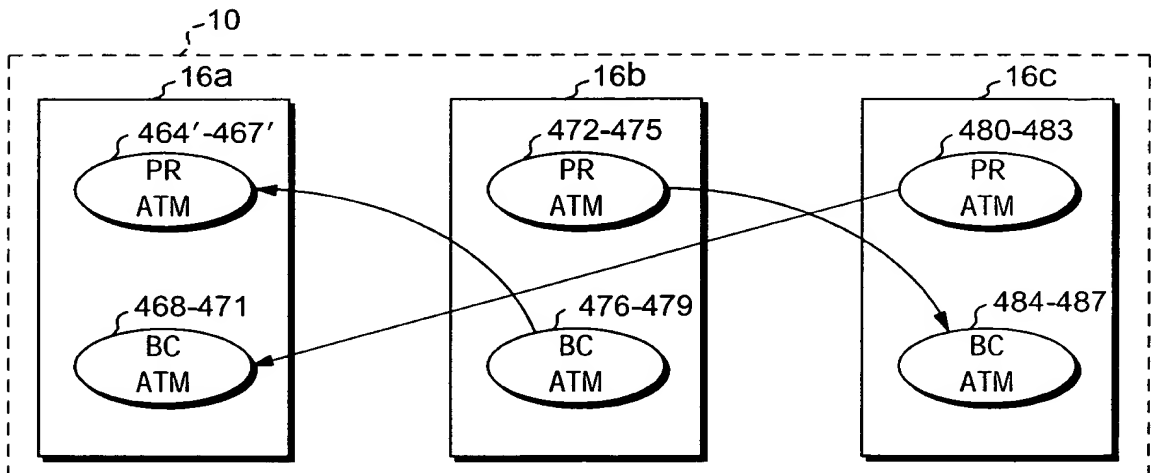


FIG. 31C

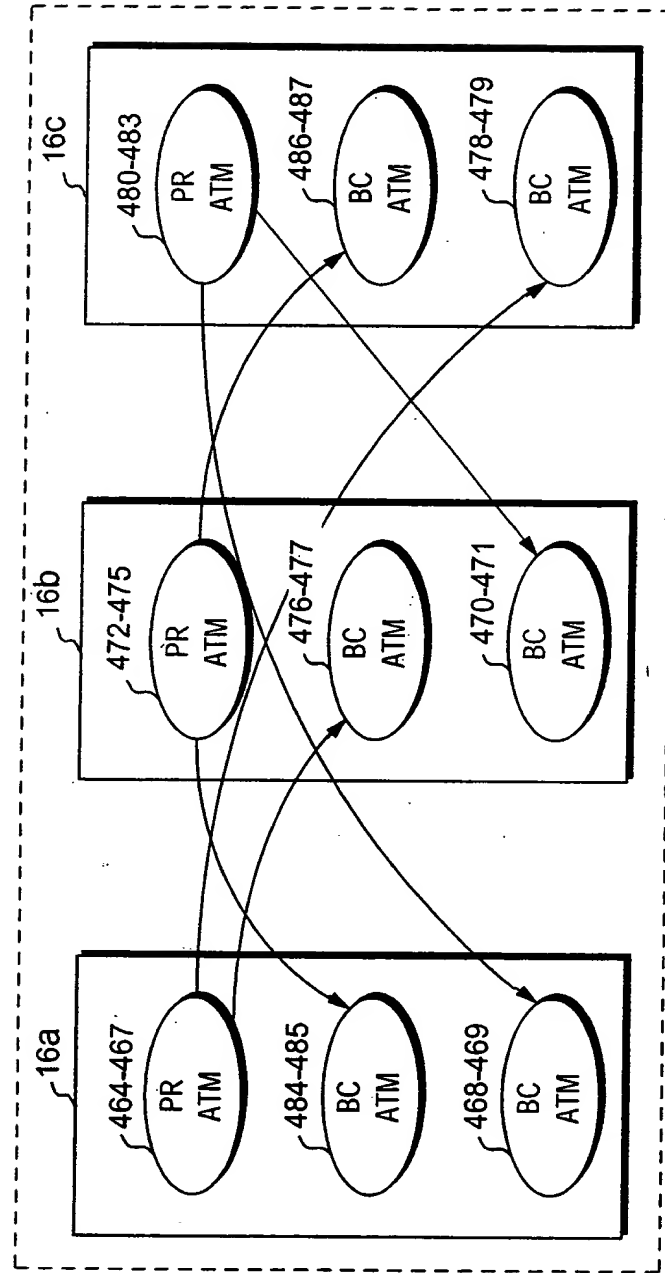


FIG. 32A

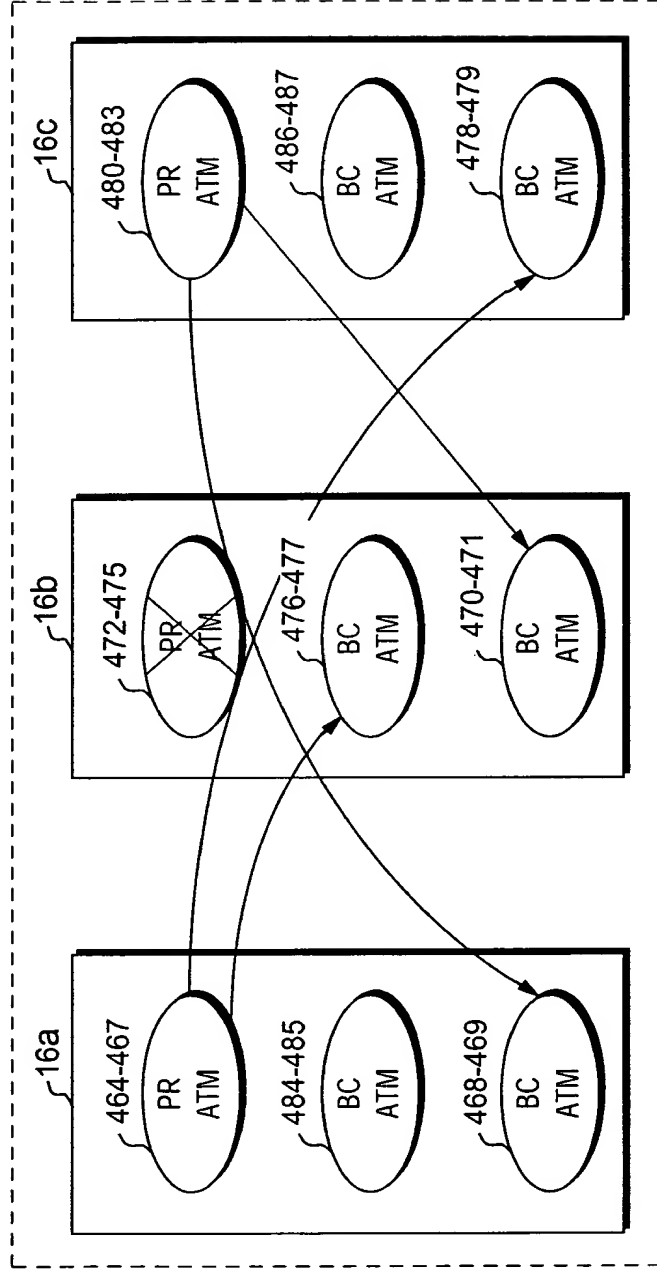


FIG. 32B

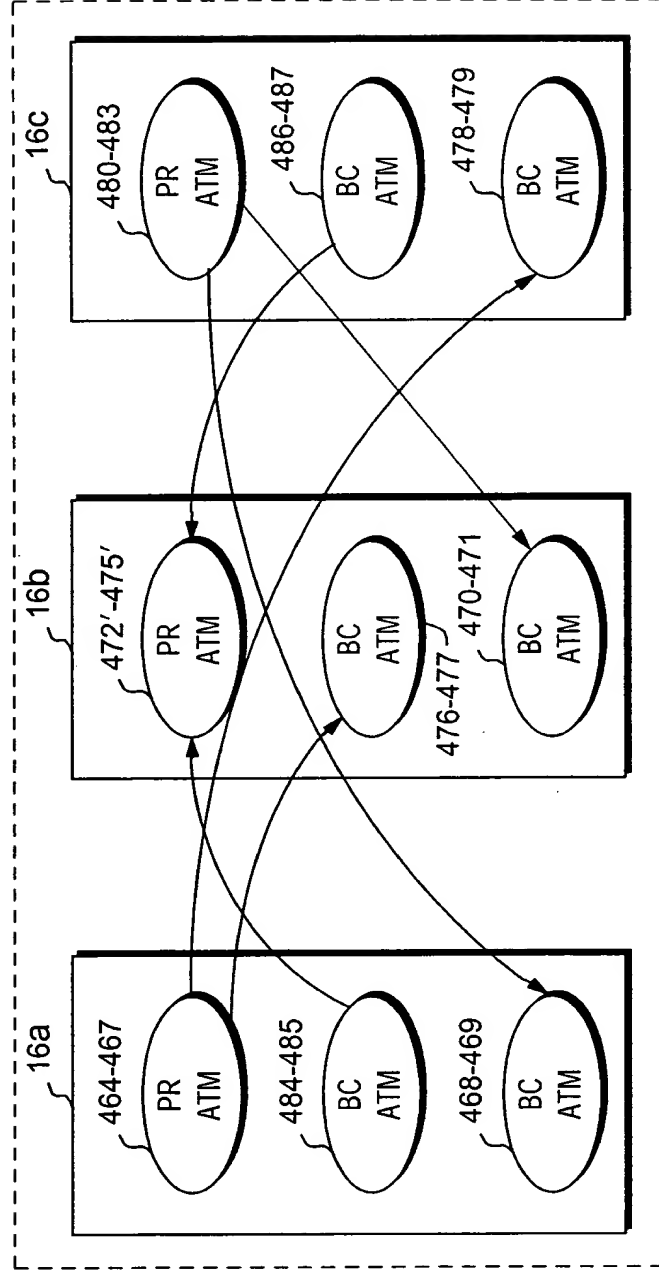


FIG. 32C

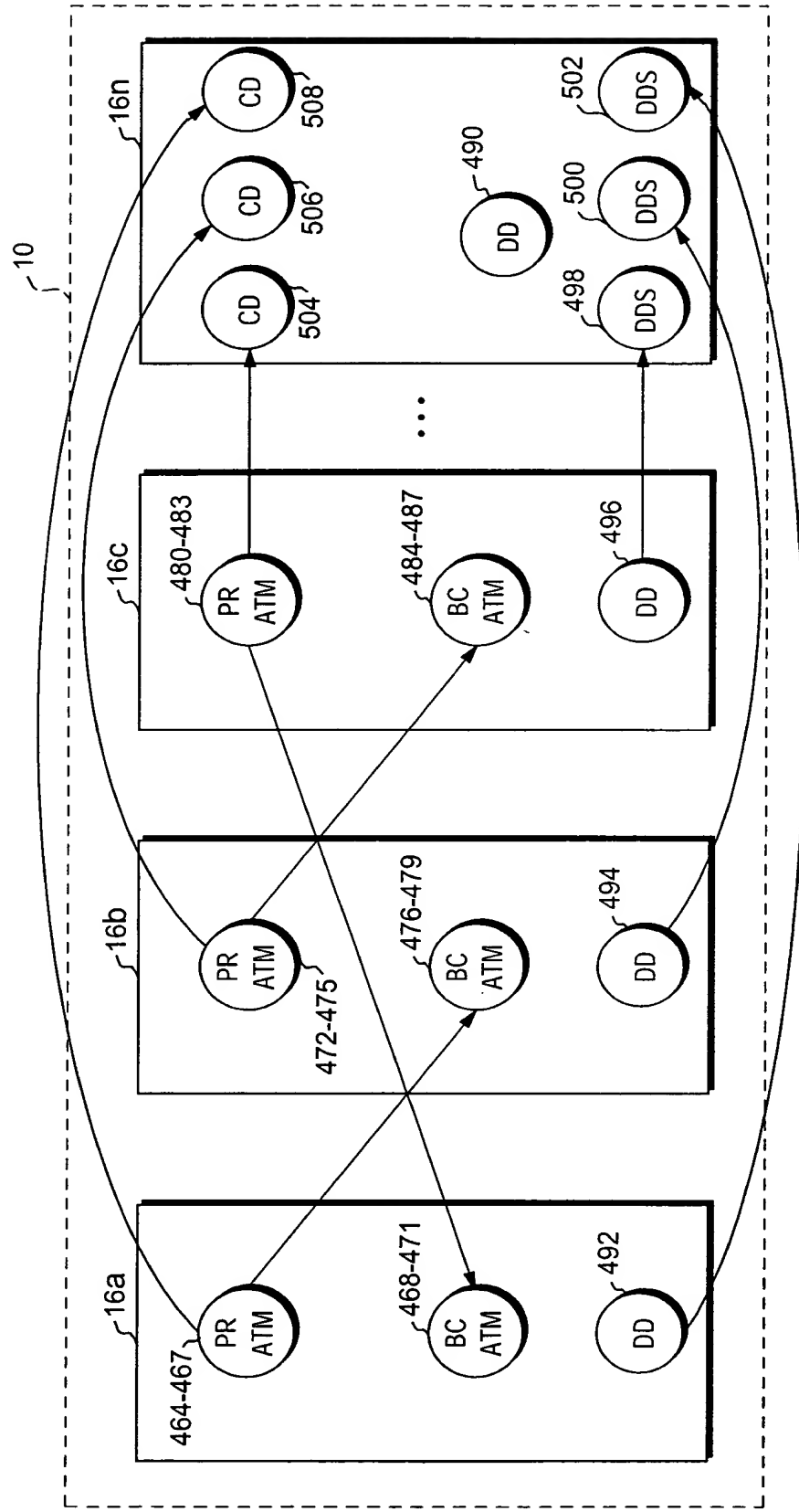


FIG. 33A

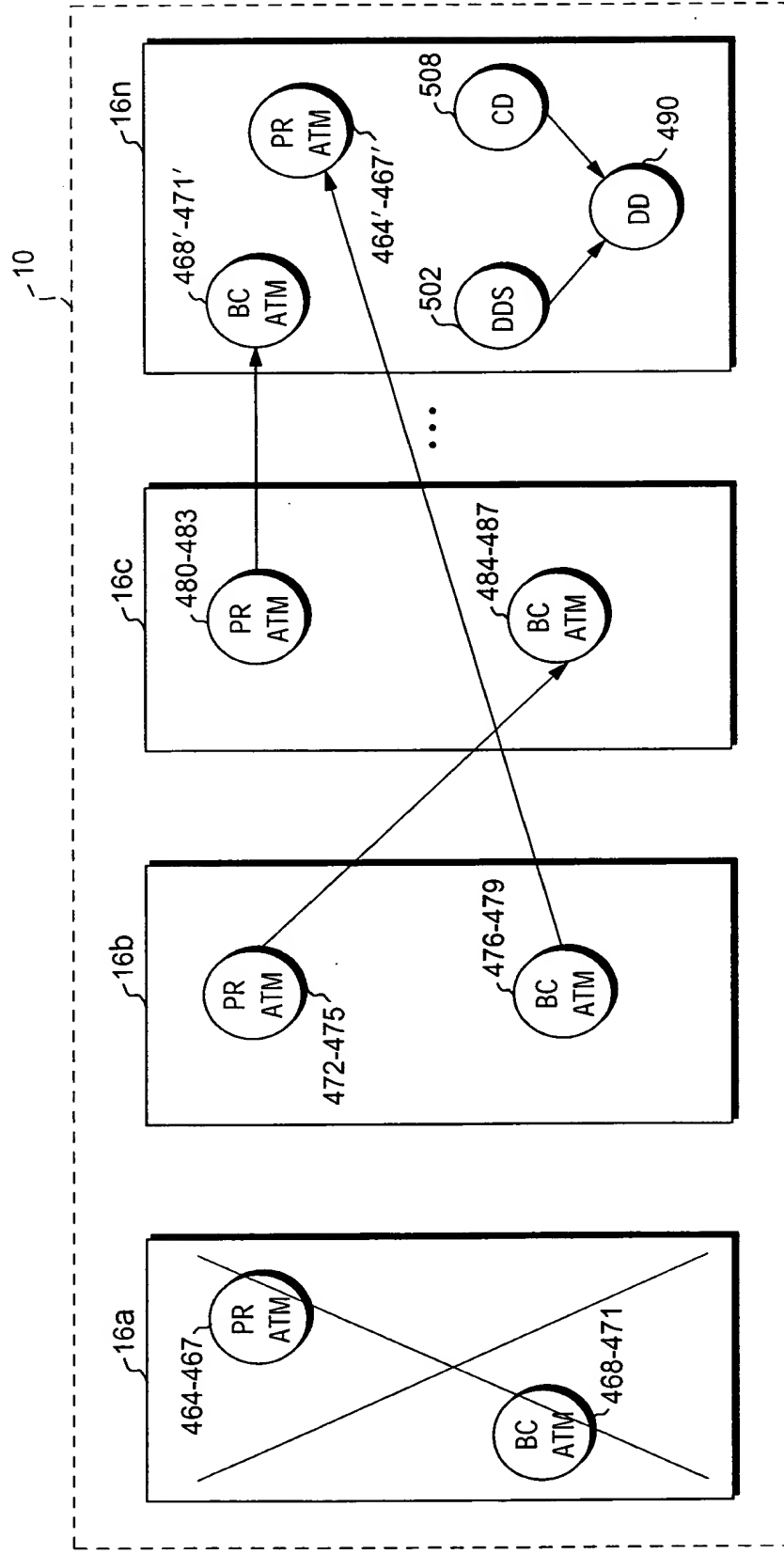


FIG. 33B

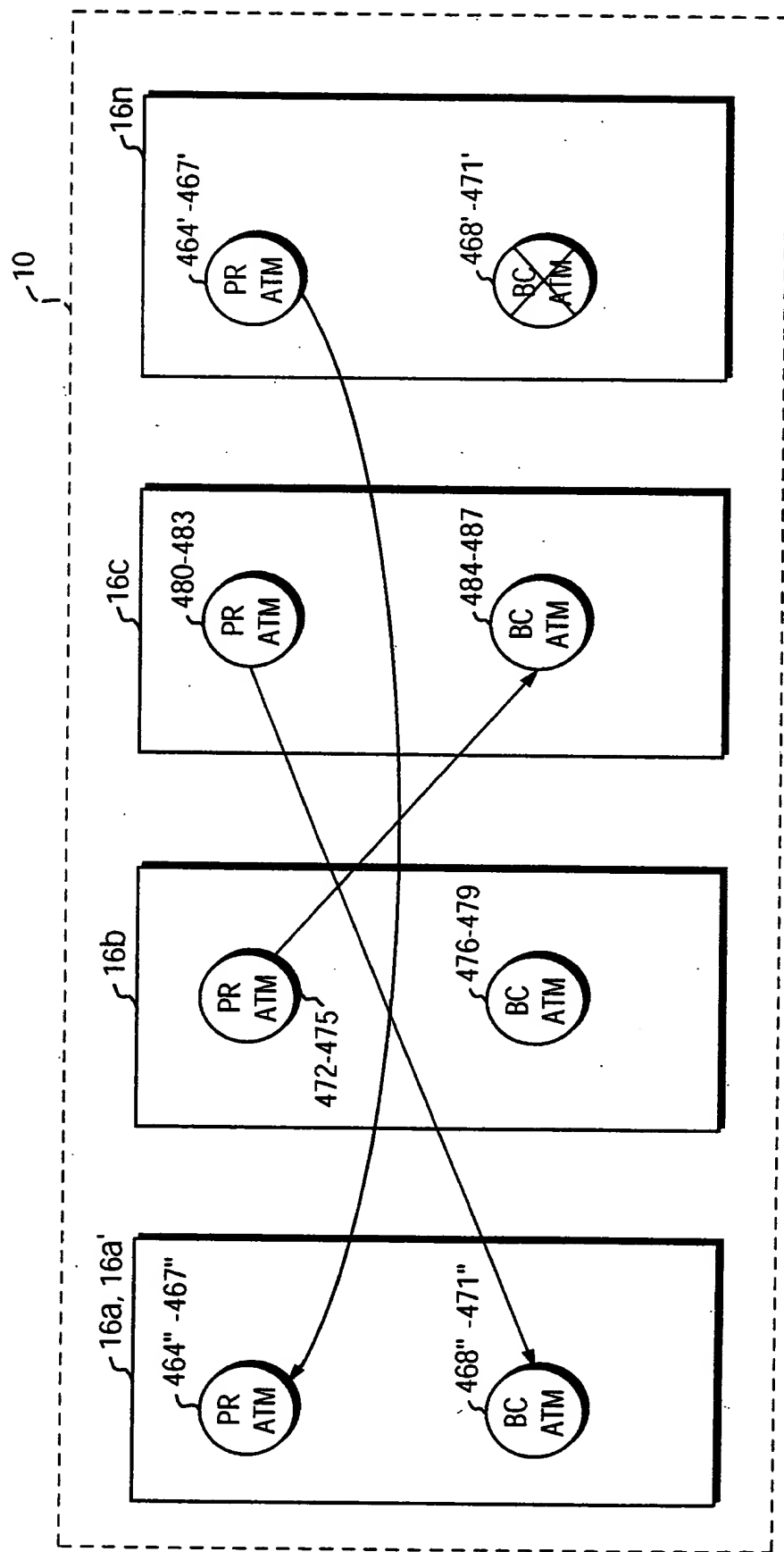


FIG. 33C

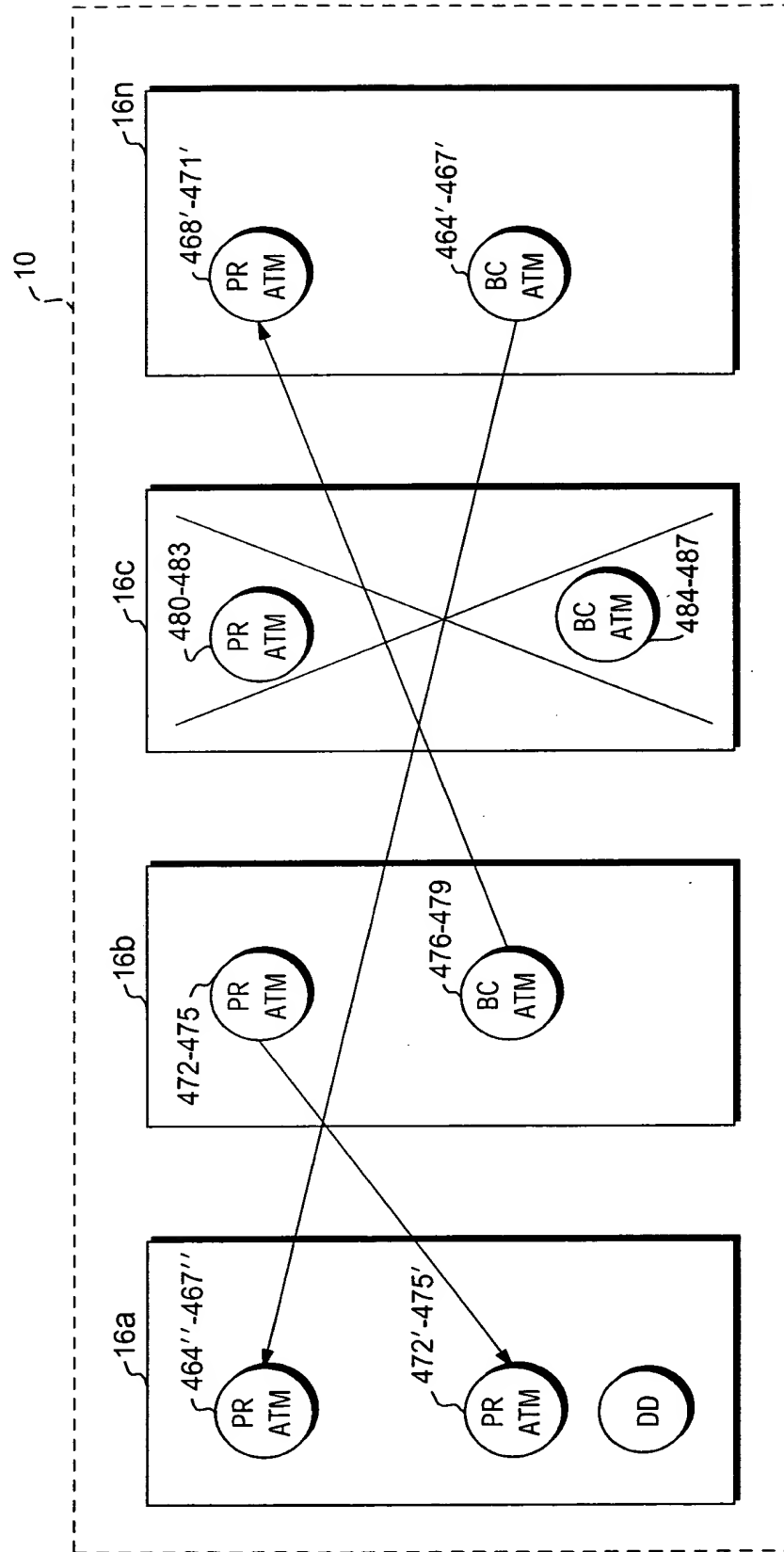


FIG. 33D

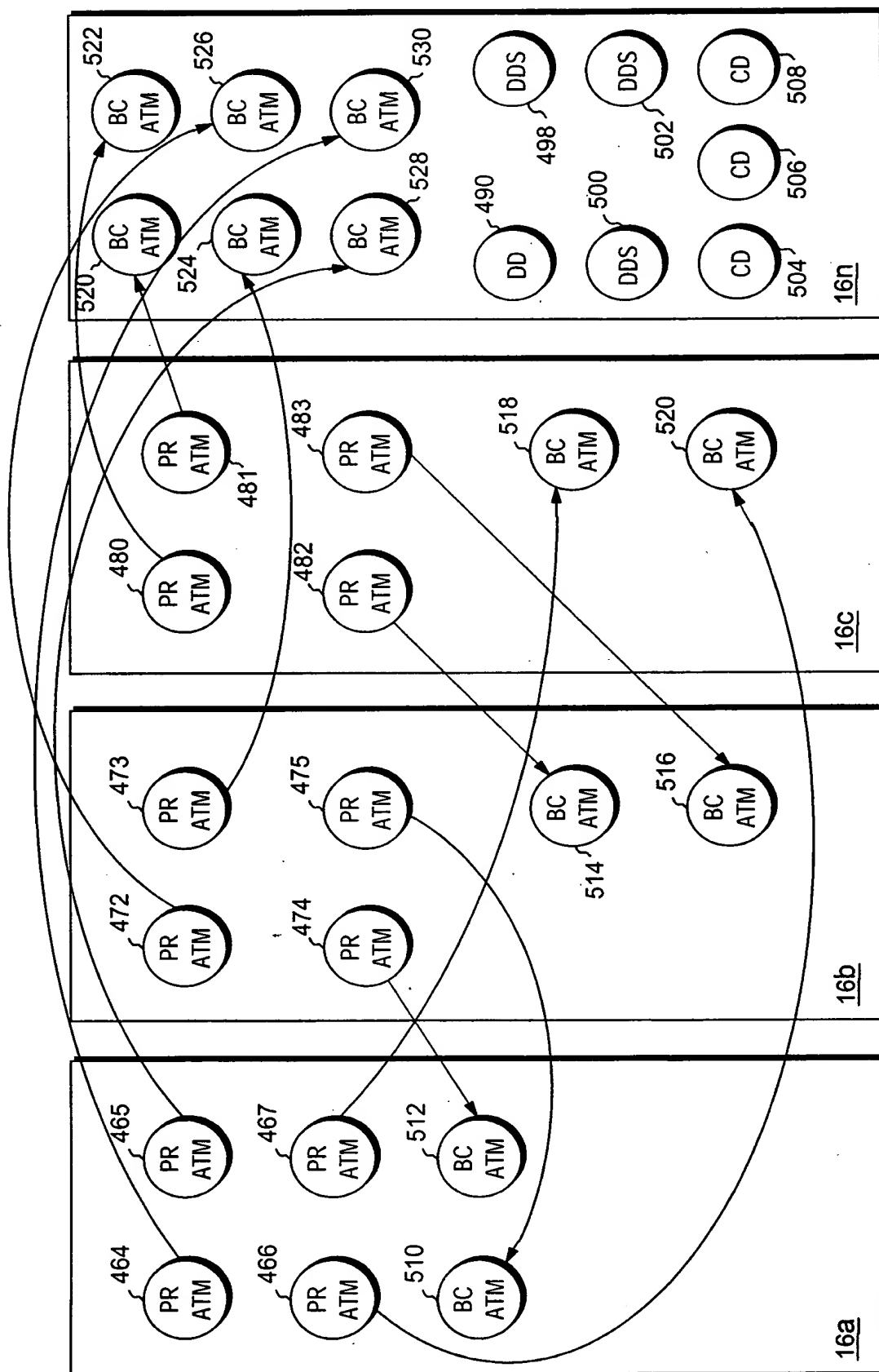


FIG. 34A

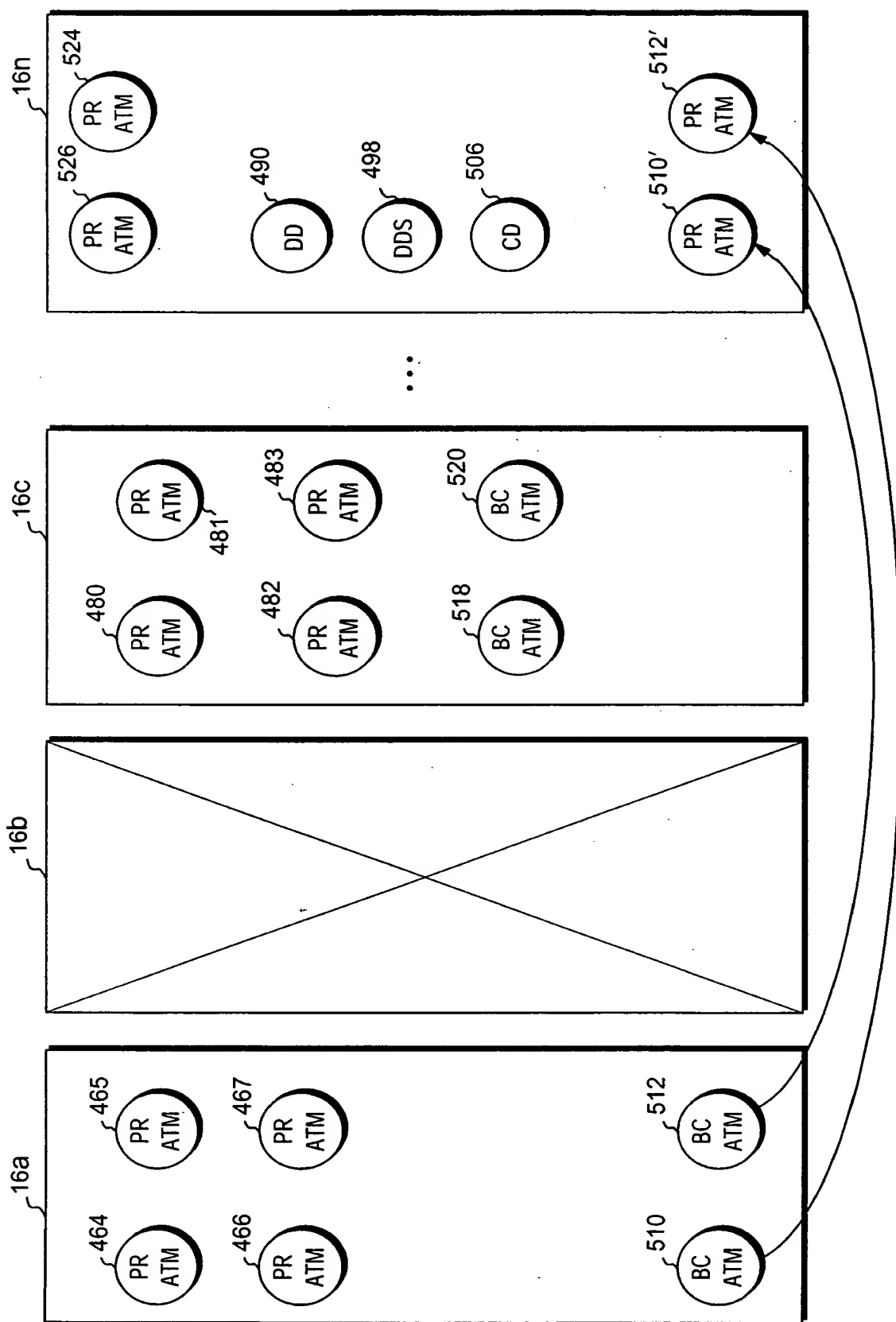


FIG. 34B

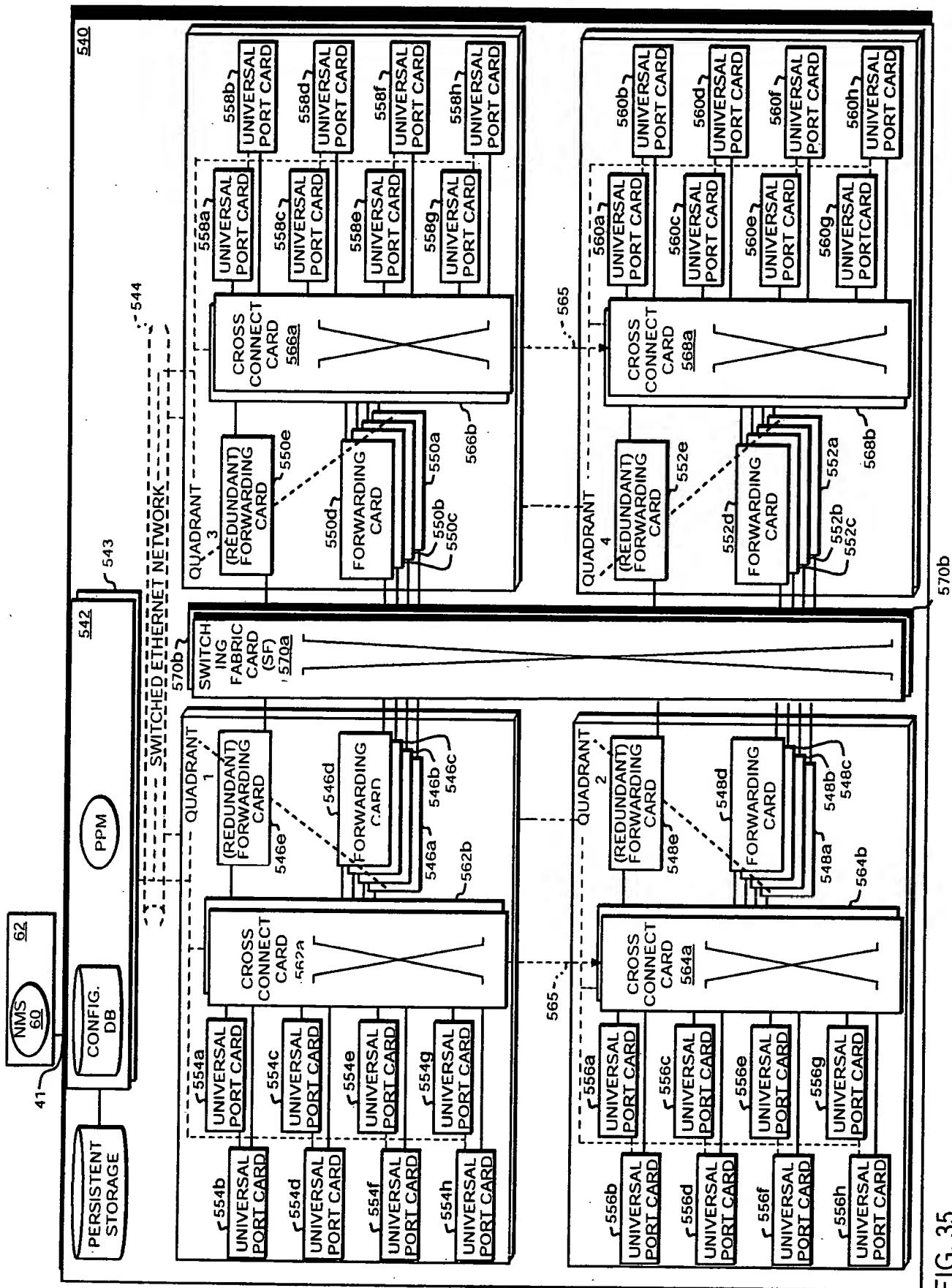


FIG. 35

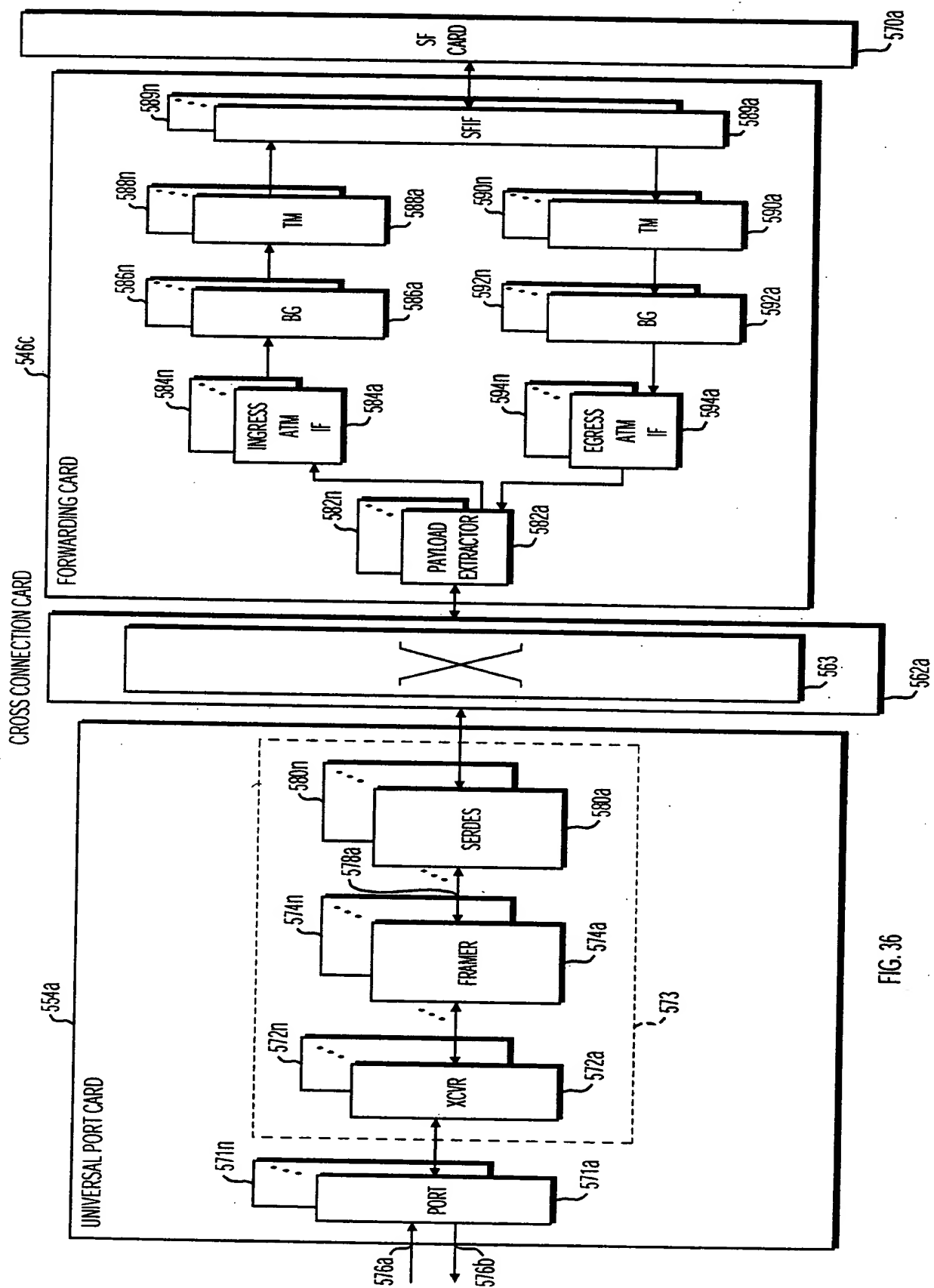


FIG. 36

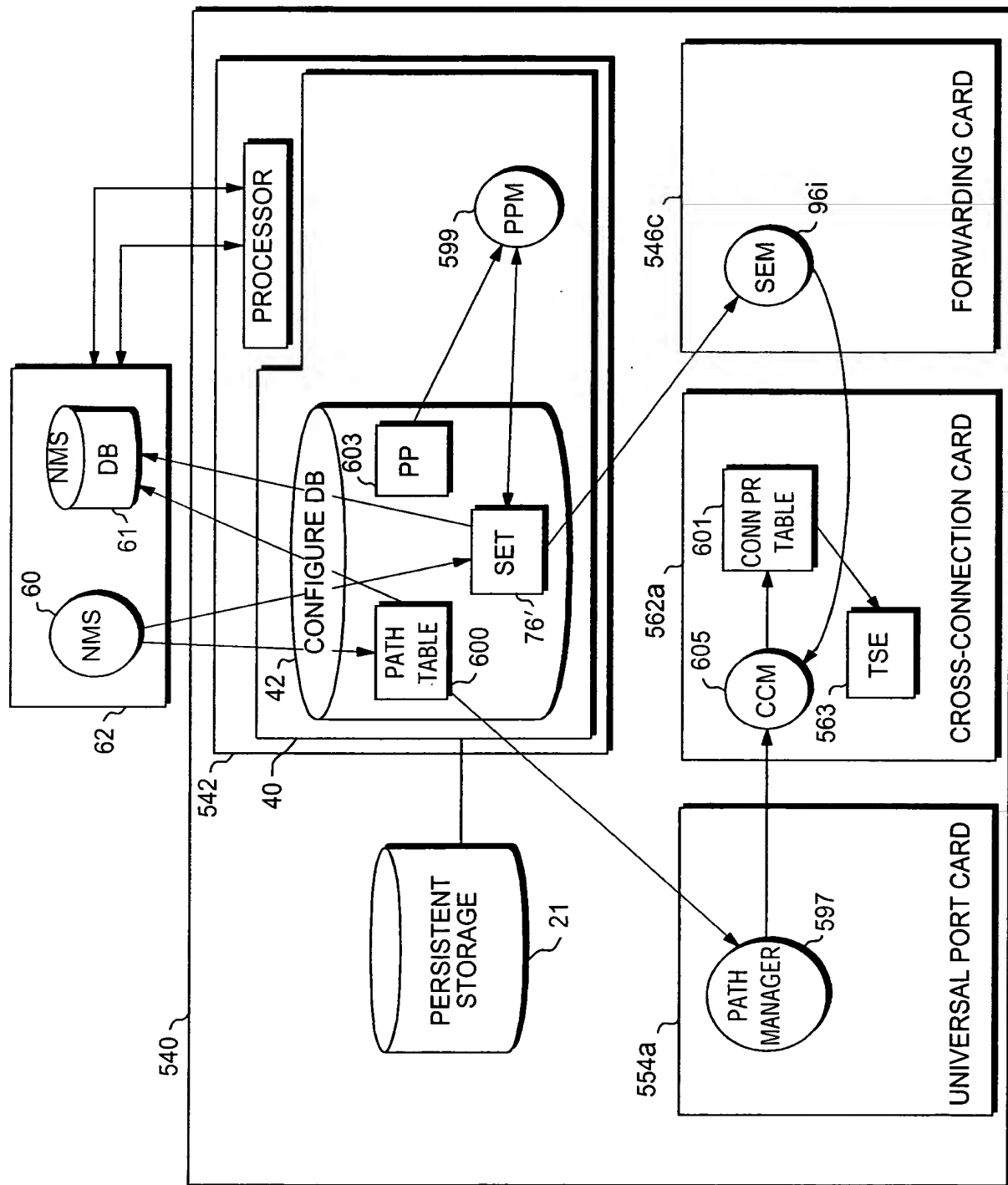


FIG. 37

PATH TABLE 600

602

PATH LID	UP PORT LID	TIME SLOT	# OF TIME SLOTS	...
1666	1231	4	3	
⋮	⋮	⋮	⋮	⋮

FIG. 38

SERVICE END POINT TABLE 76'

			606	608	610		
	SE #	Q #	FC LID	FC SLICE	FC TIME SLOT	PATH PID	...
604	878	1				1666	
	⋮	⋮	⋮	⋮	⋮	⋮	⋮

FIG. 39

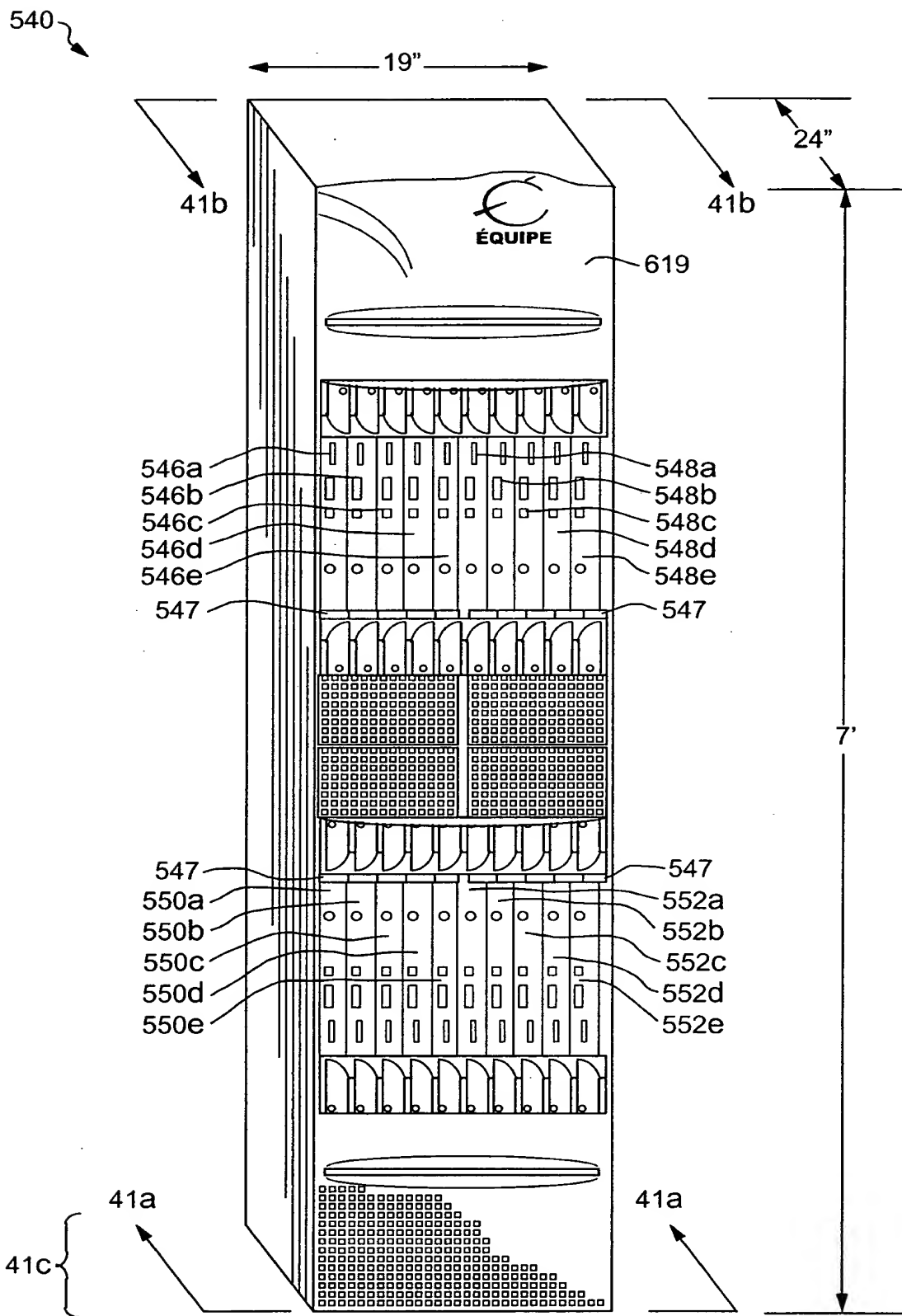


FIG. 40

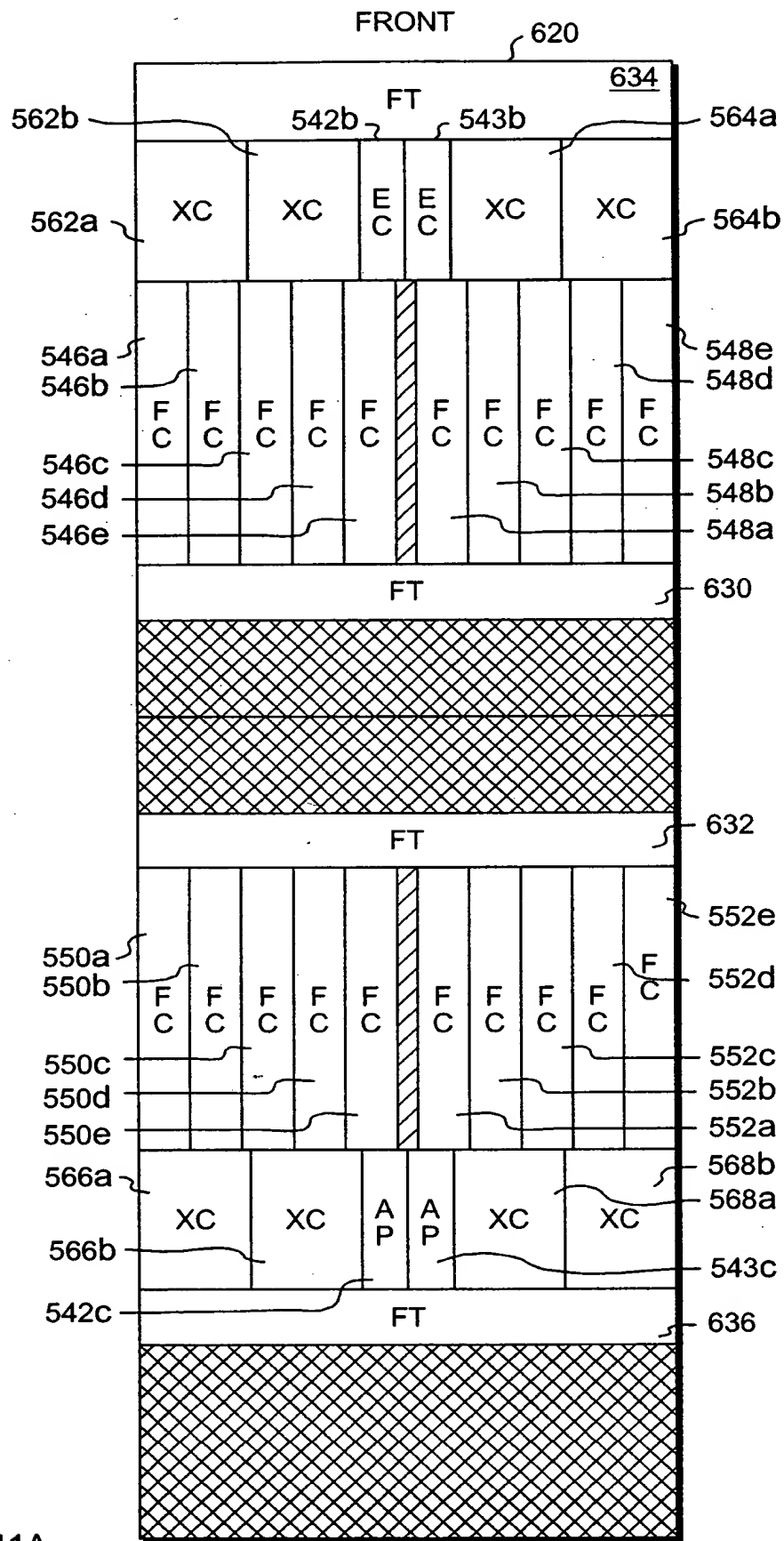


FIG. 41A

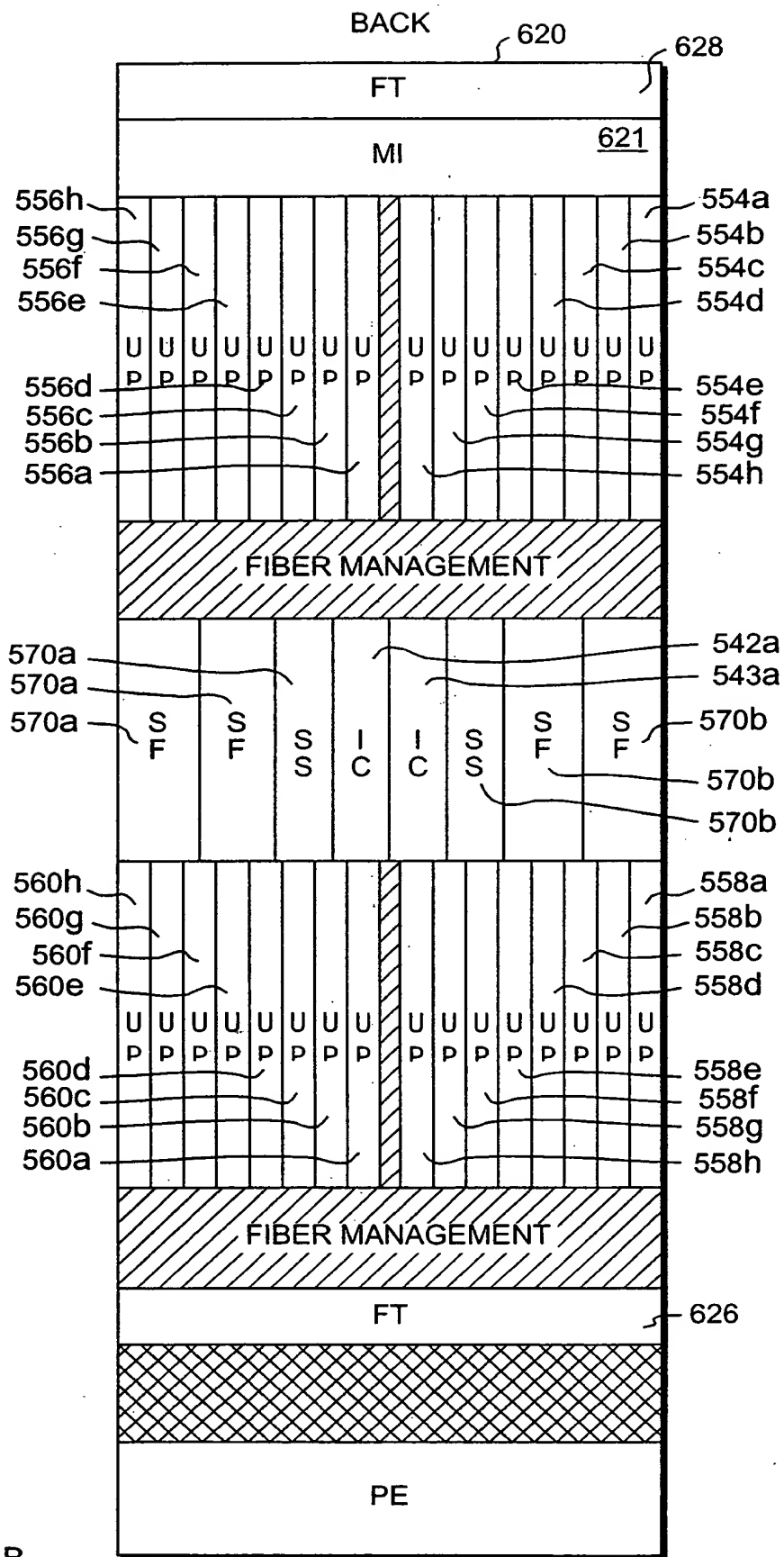


FIG. 41B

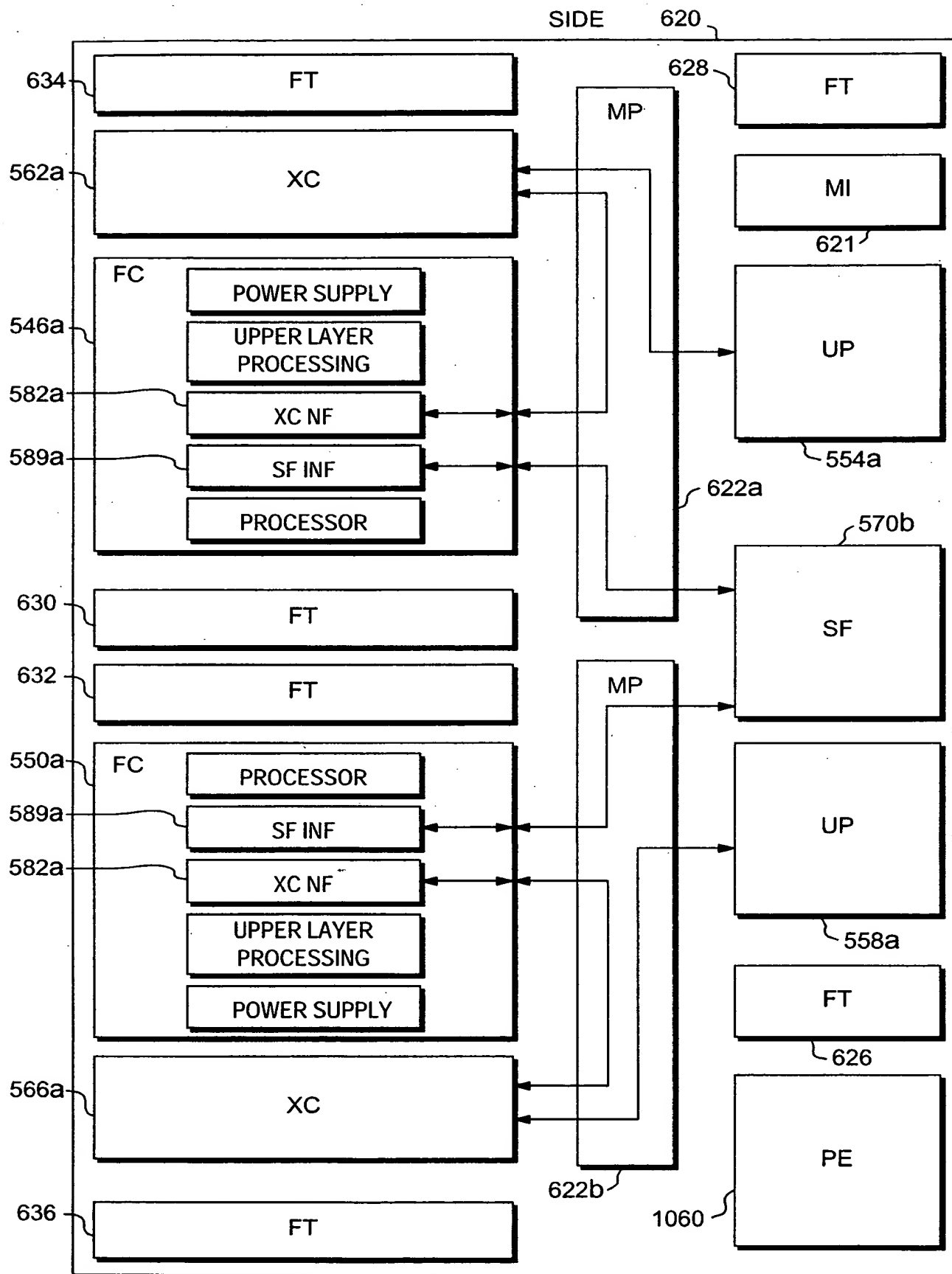


FIG. 41C

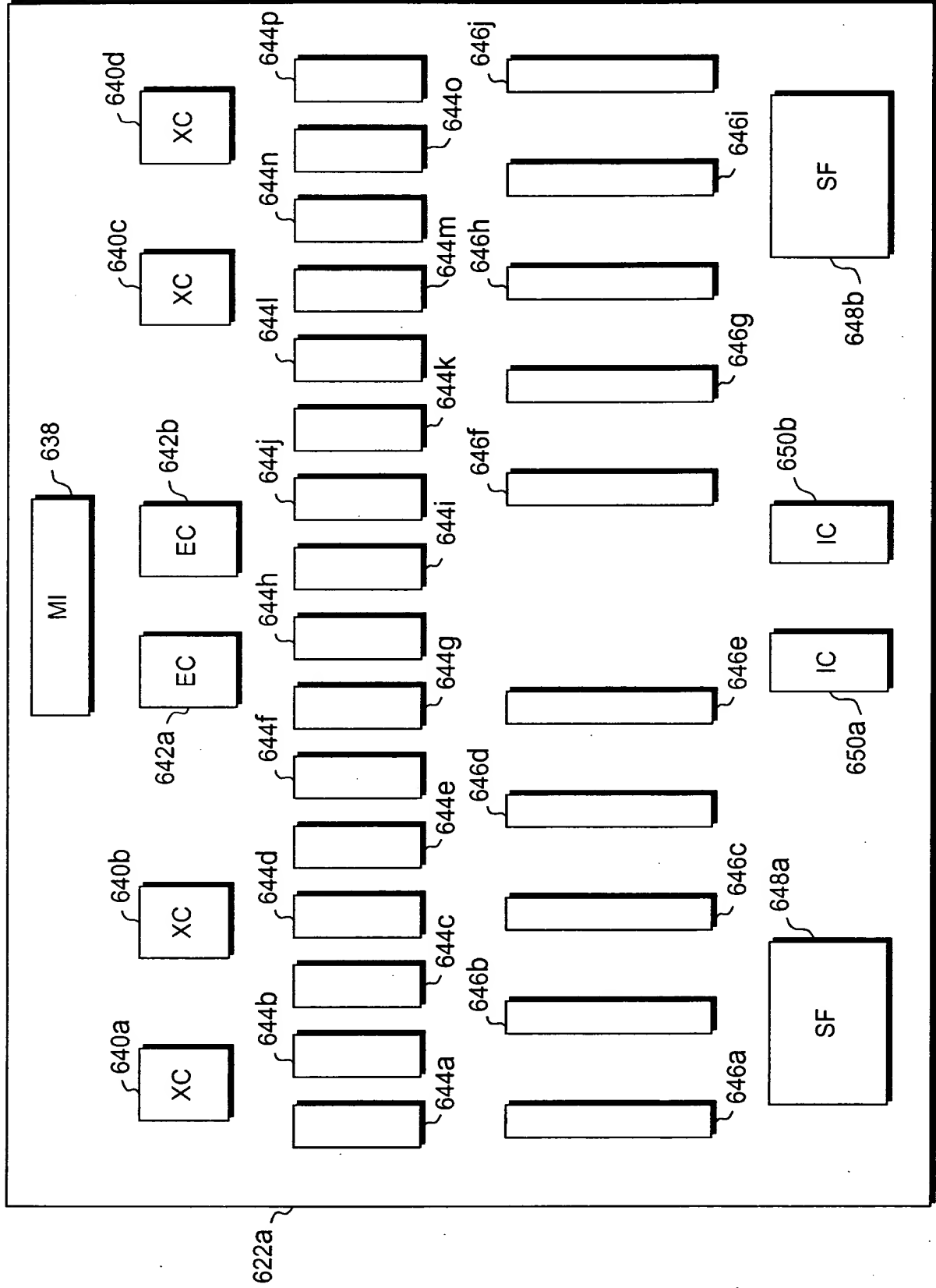


FIG. 42A

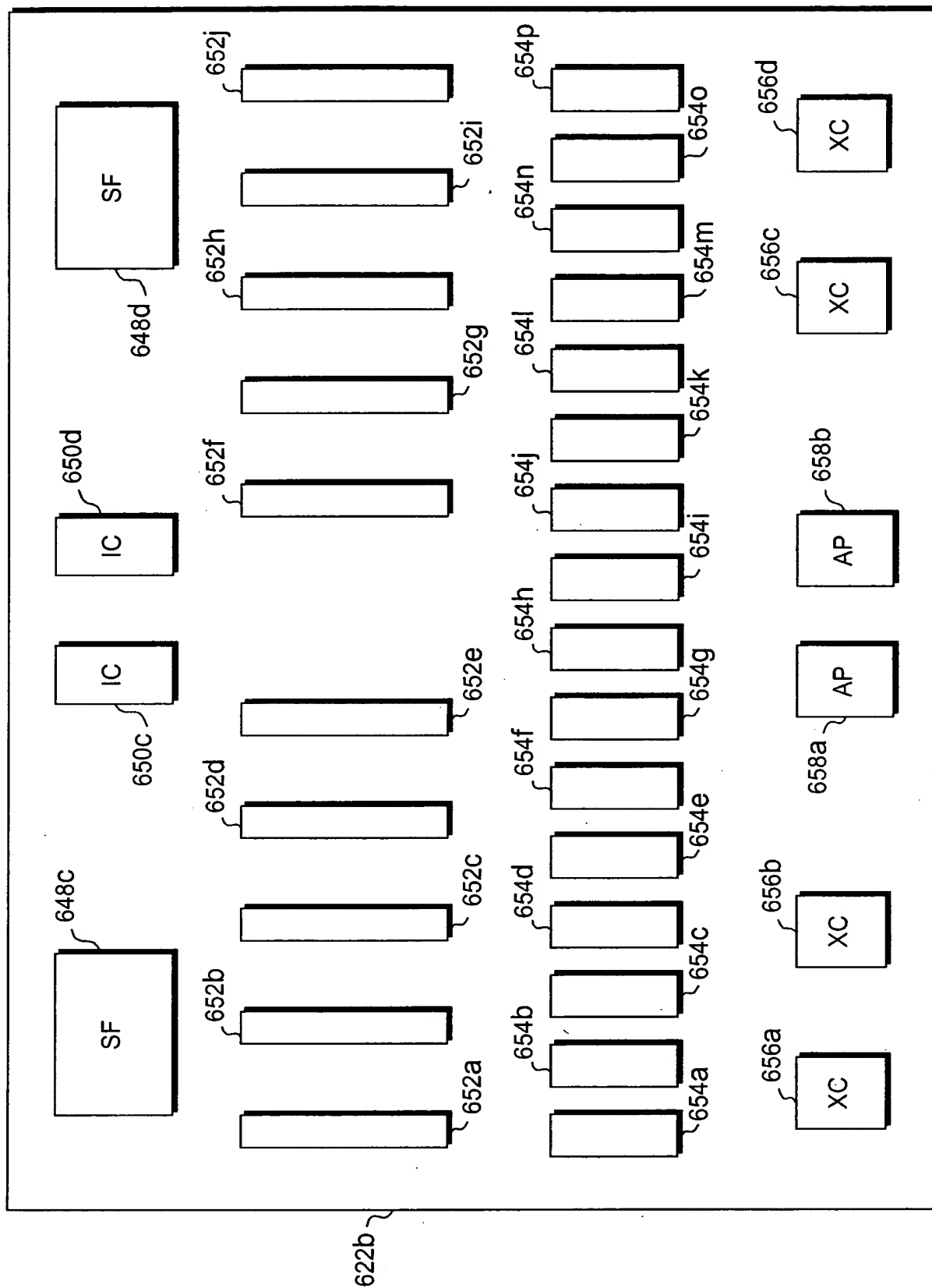


FIG. 42B

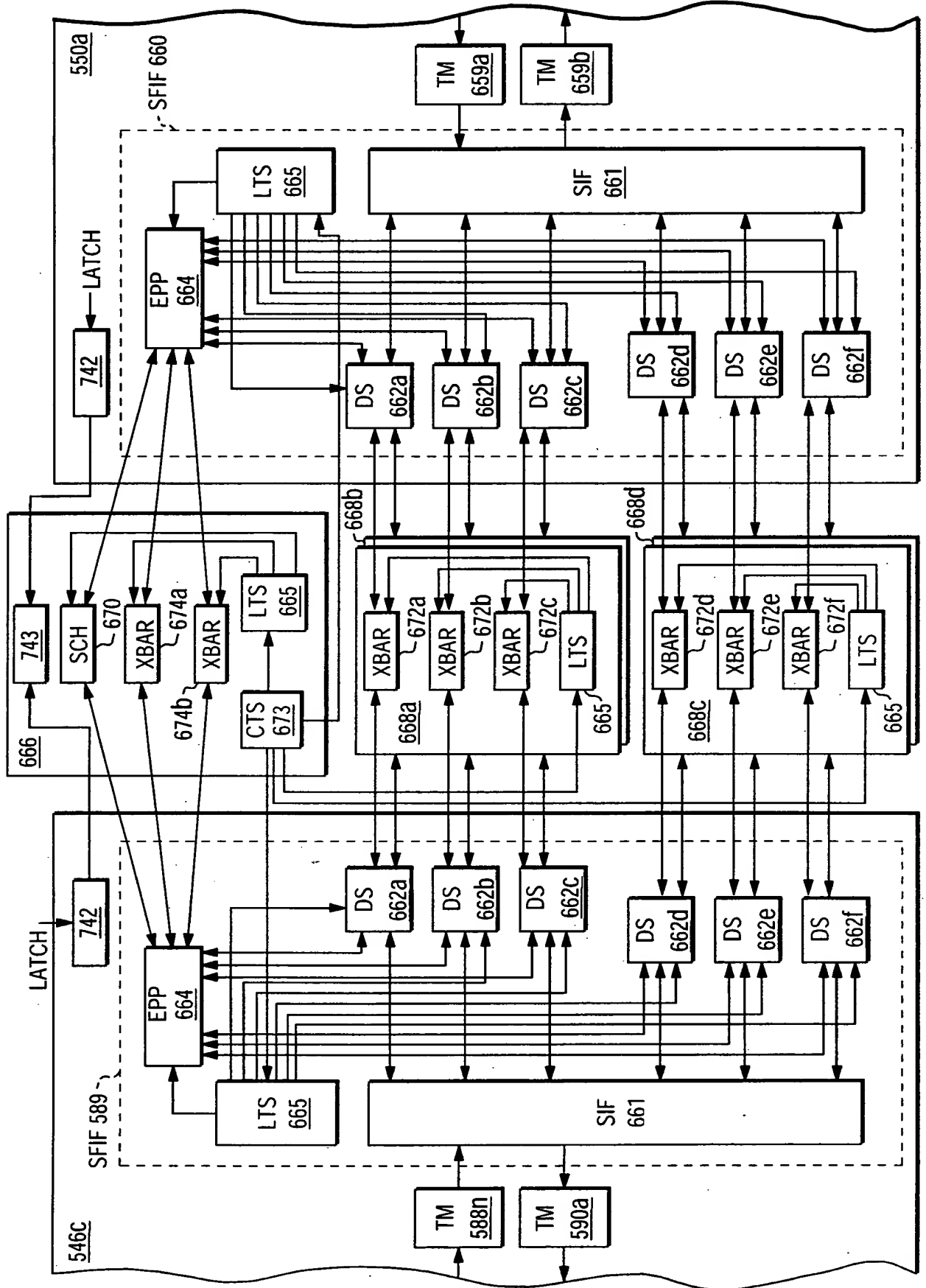


FIG. 43

540

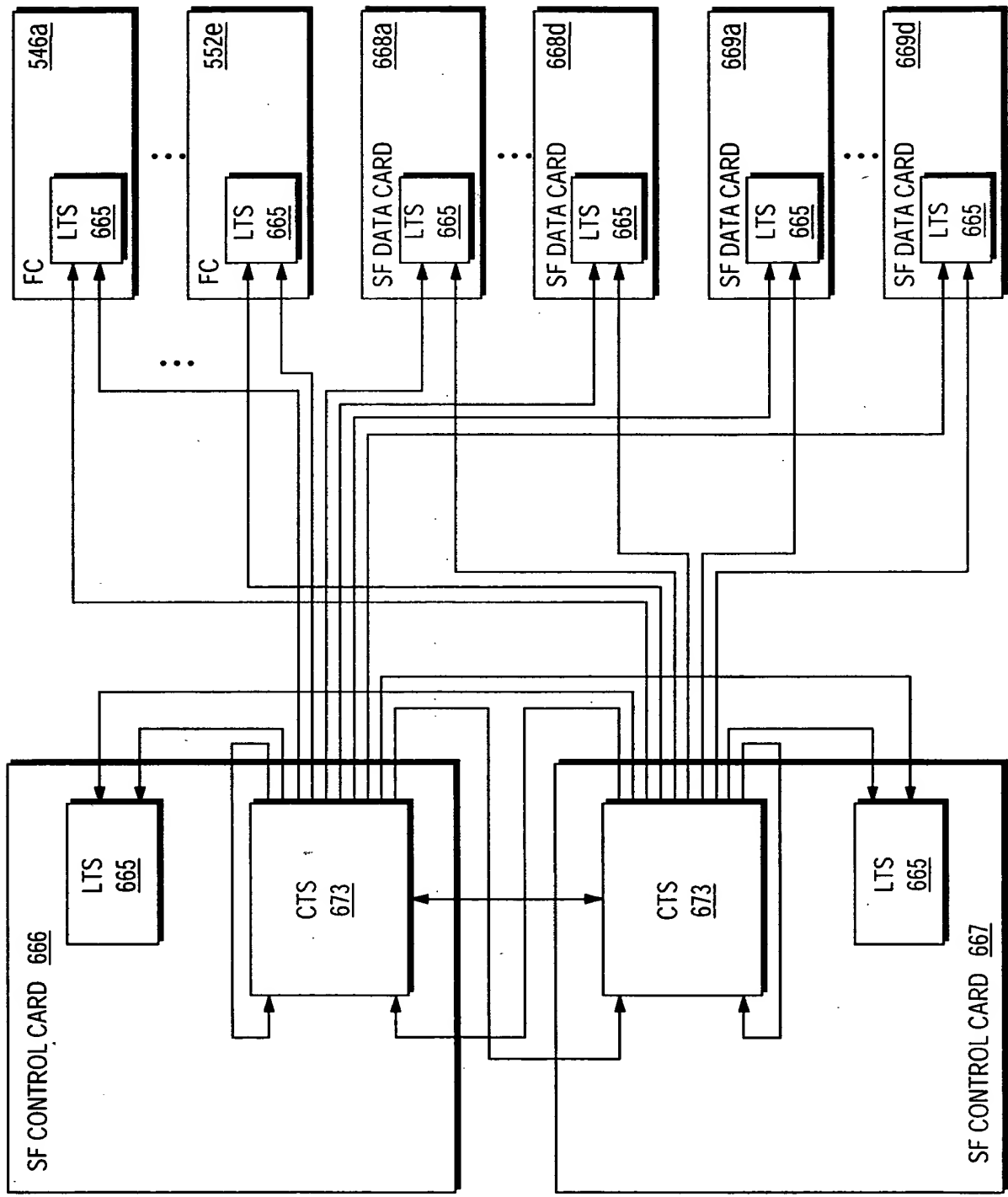


FIG. 44

CTS
673

TO 682a
FIG. 45B

TO 680
FIG. 45B

TO 709
FIG. 45B

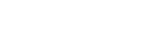
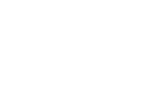
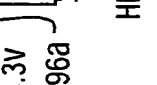
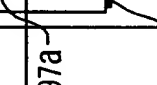
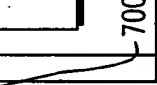
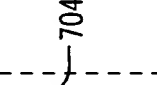
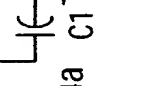
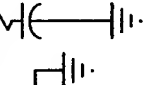
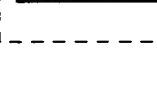
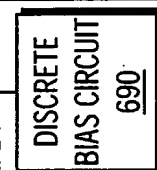
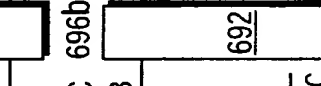
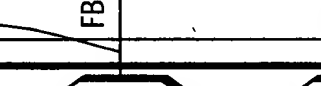
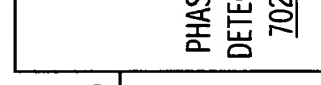
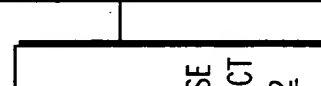
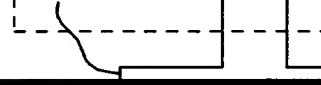
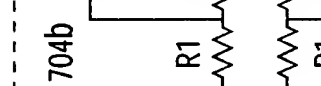
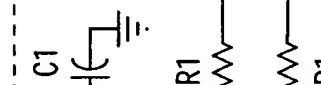
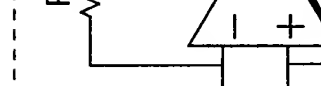
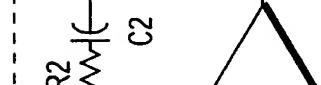
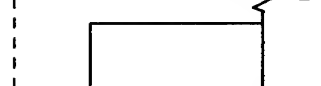
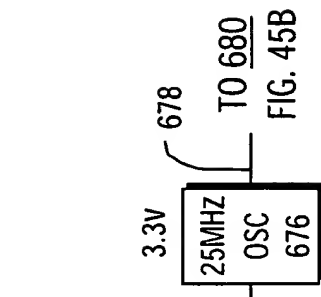
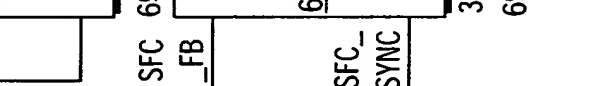
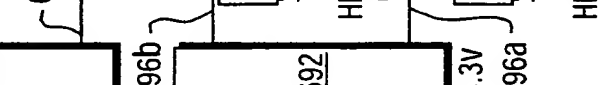
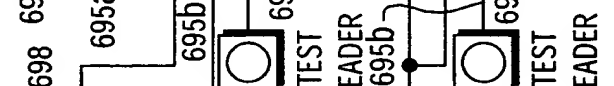
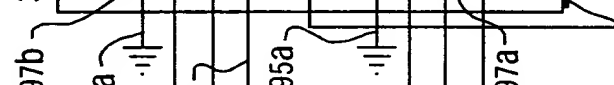
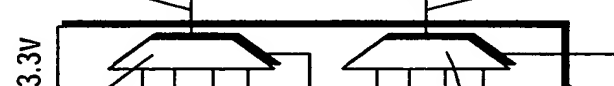
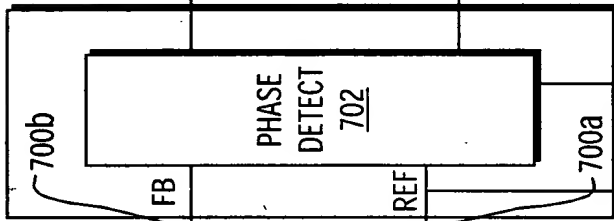
SFC_BENCH_FB

SFC_REF_ACTIVITY

FABRIC_REF_ACTIVITY

3.3V

3.3V



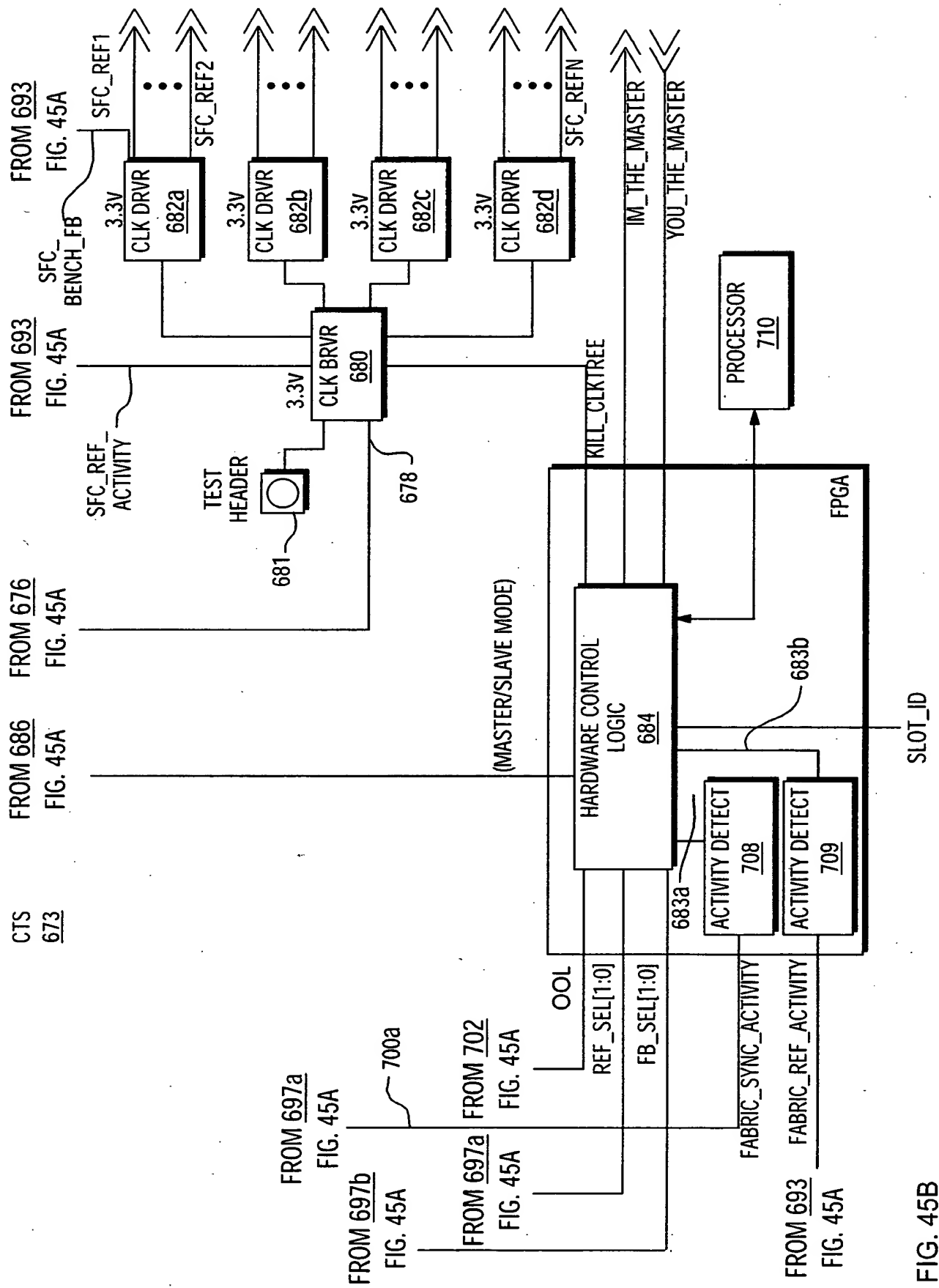
(MASTER/SLAVE MODE)

TO 684
FIG. 45B

TO 684
FIG. 45B

TO 684
FIG. 45B

FIG. 45A



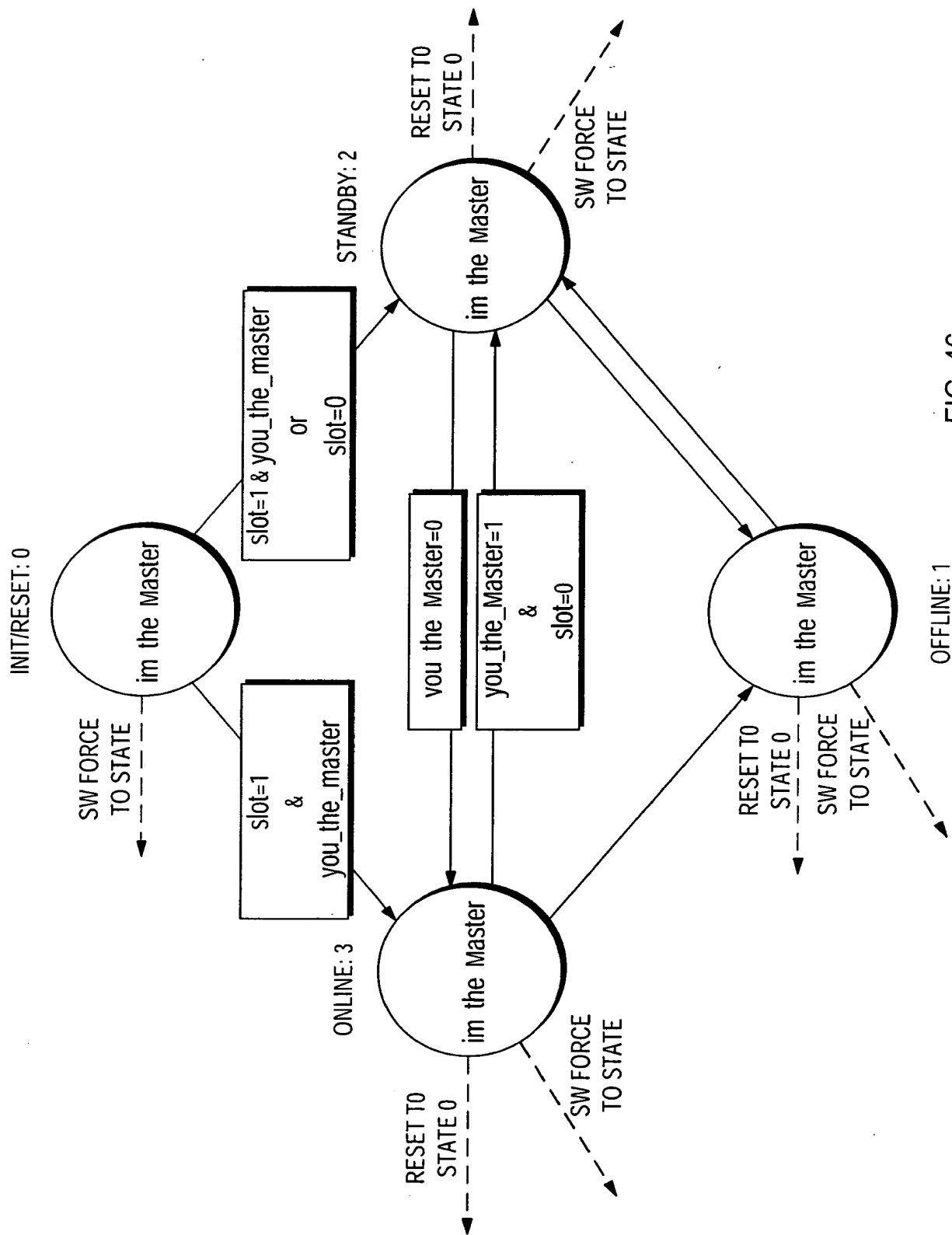


FIG. 46

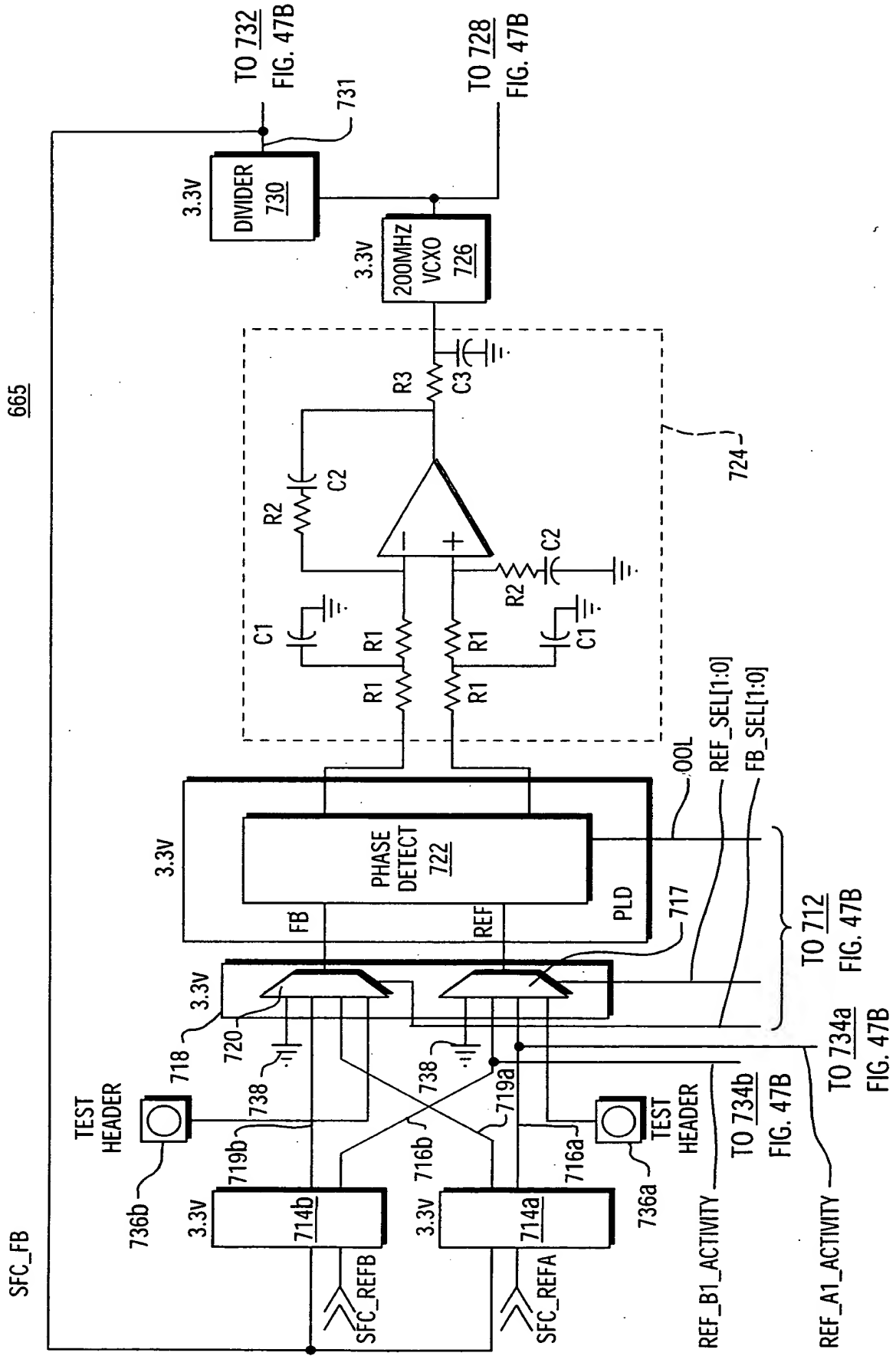


FIG. 47A

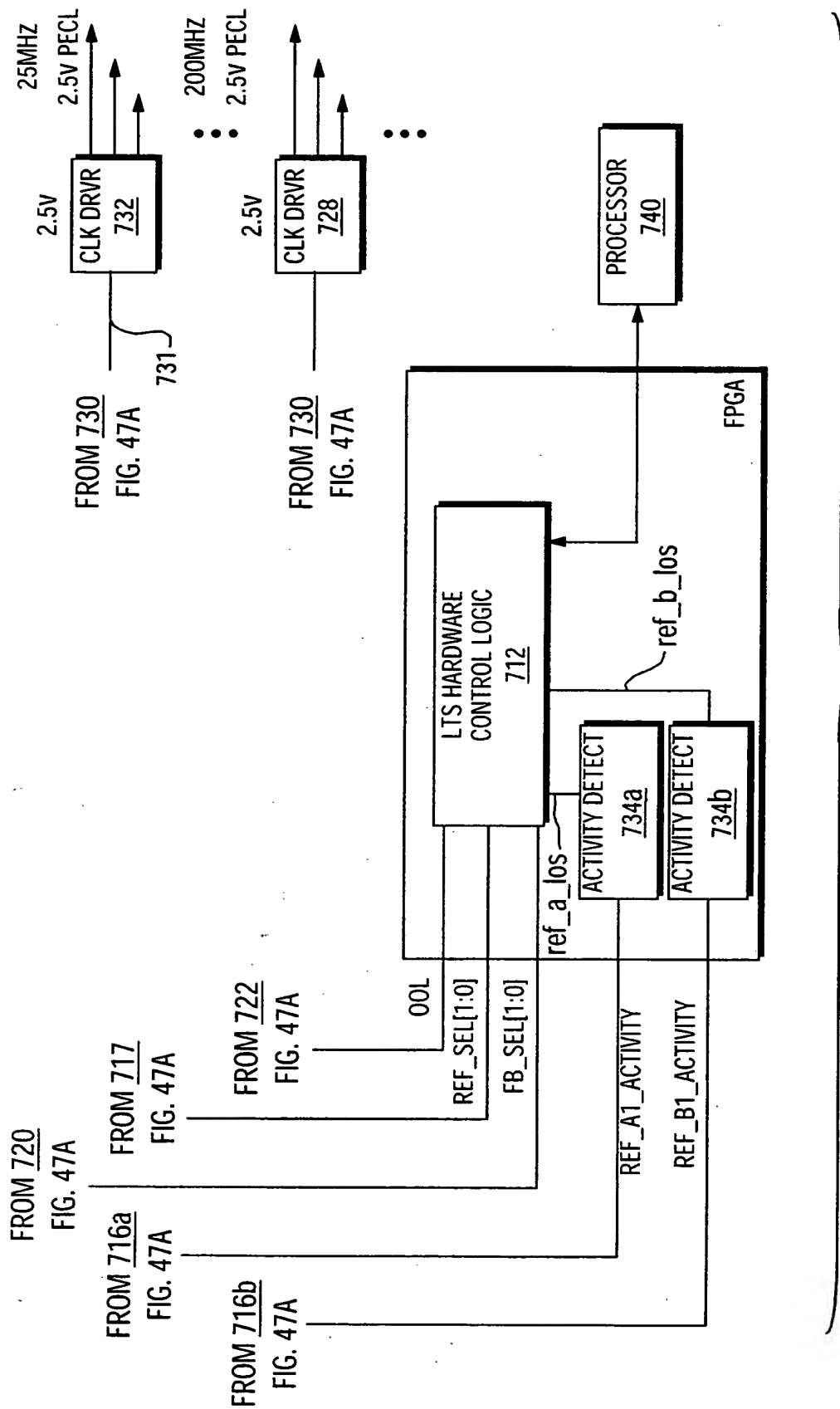


FIG. 47B

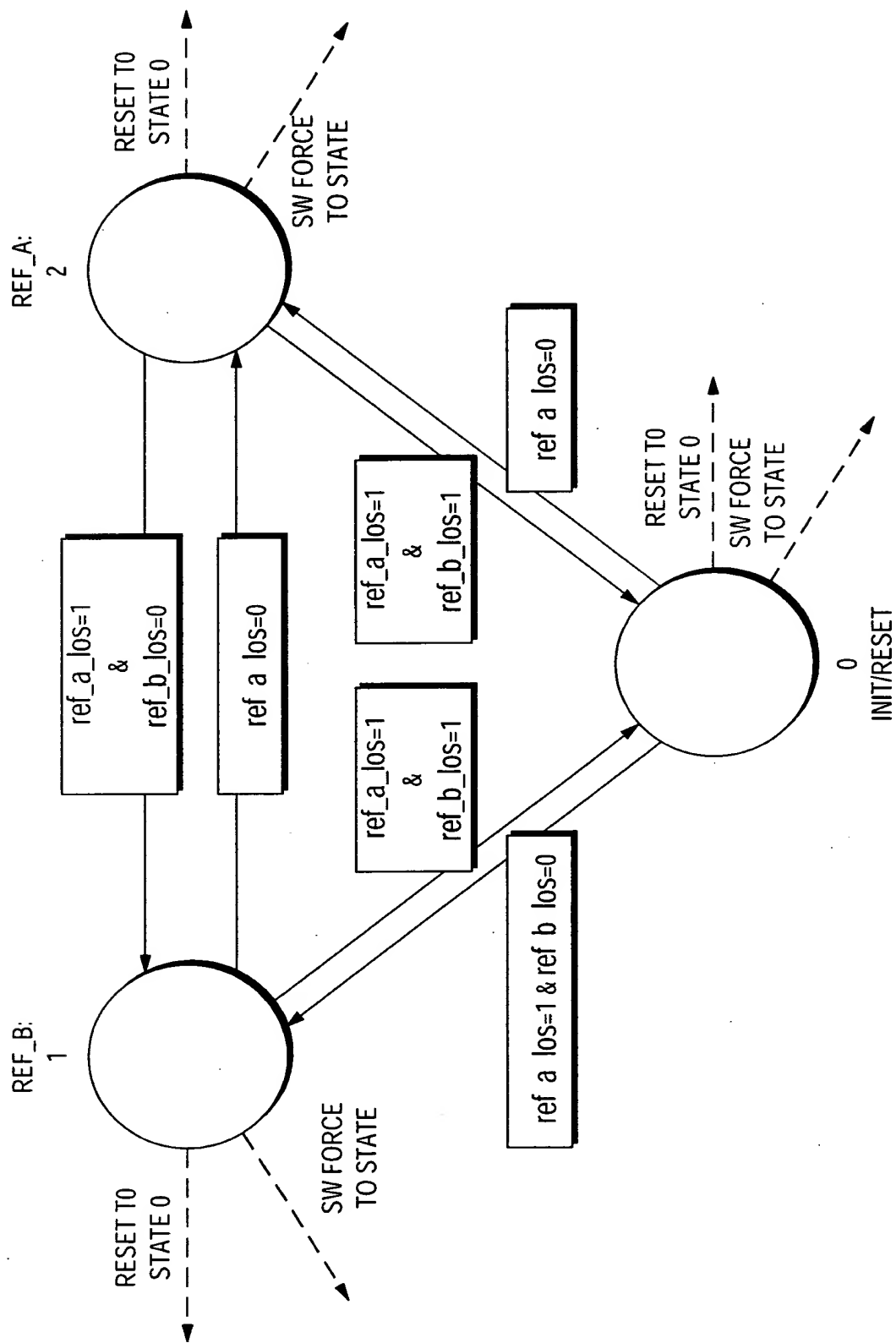


FIG. 48

540

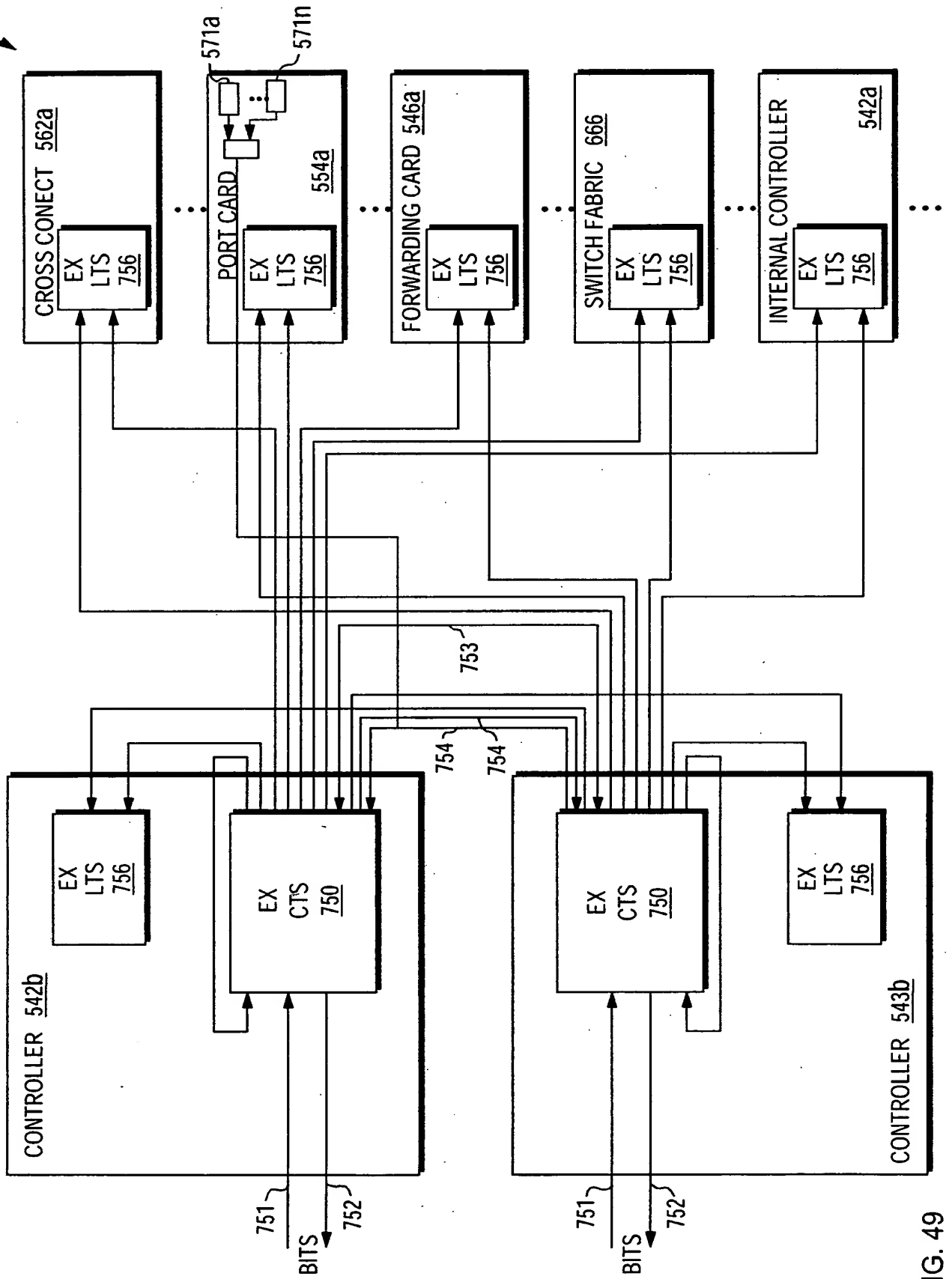


FIG. 49

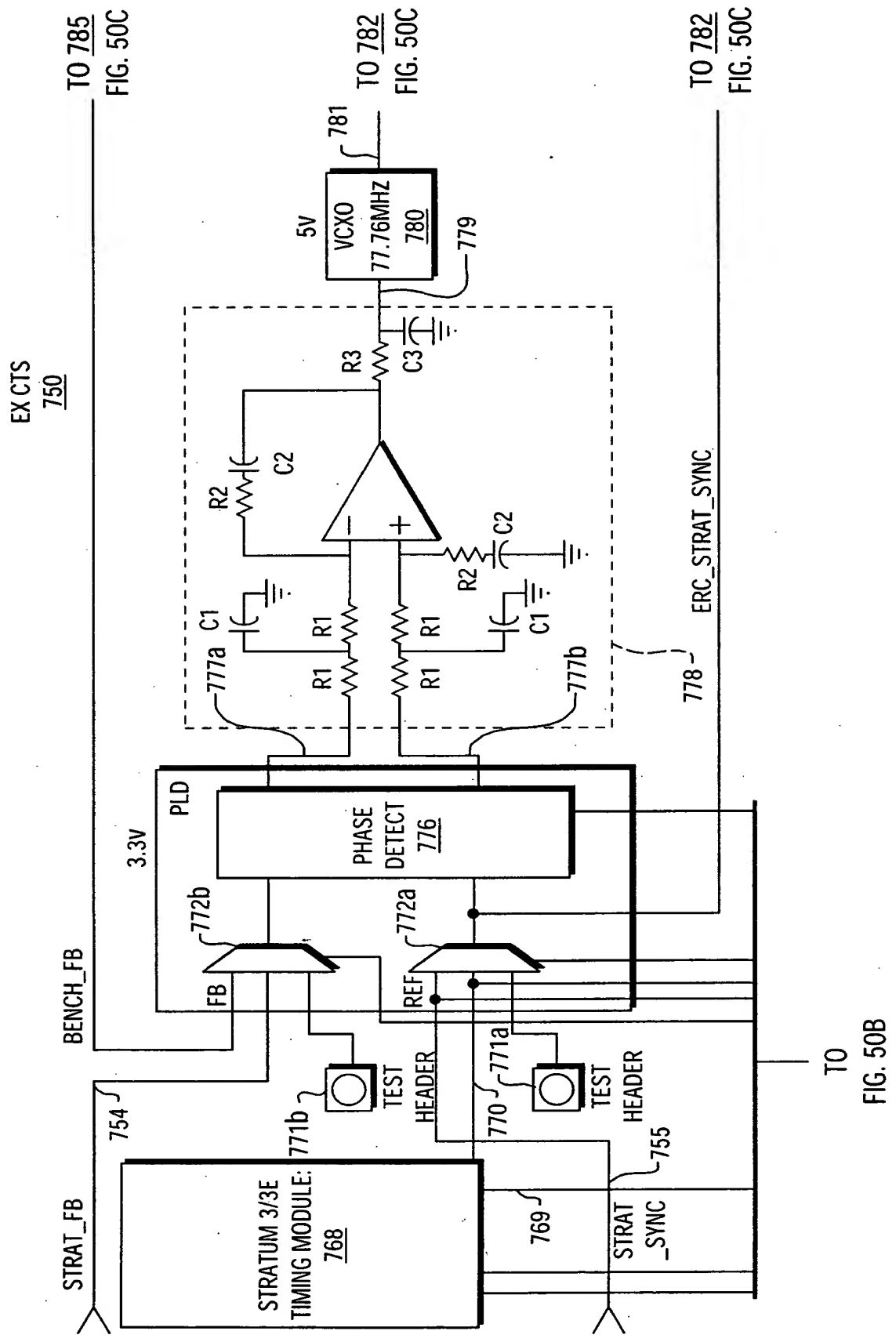


FIG. 50A

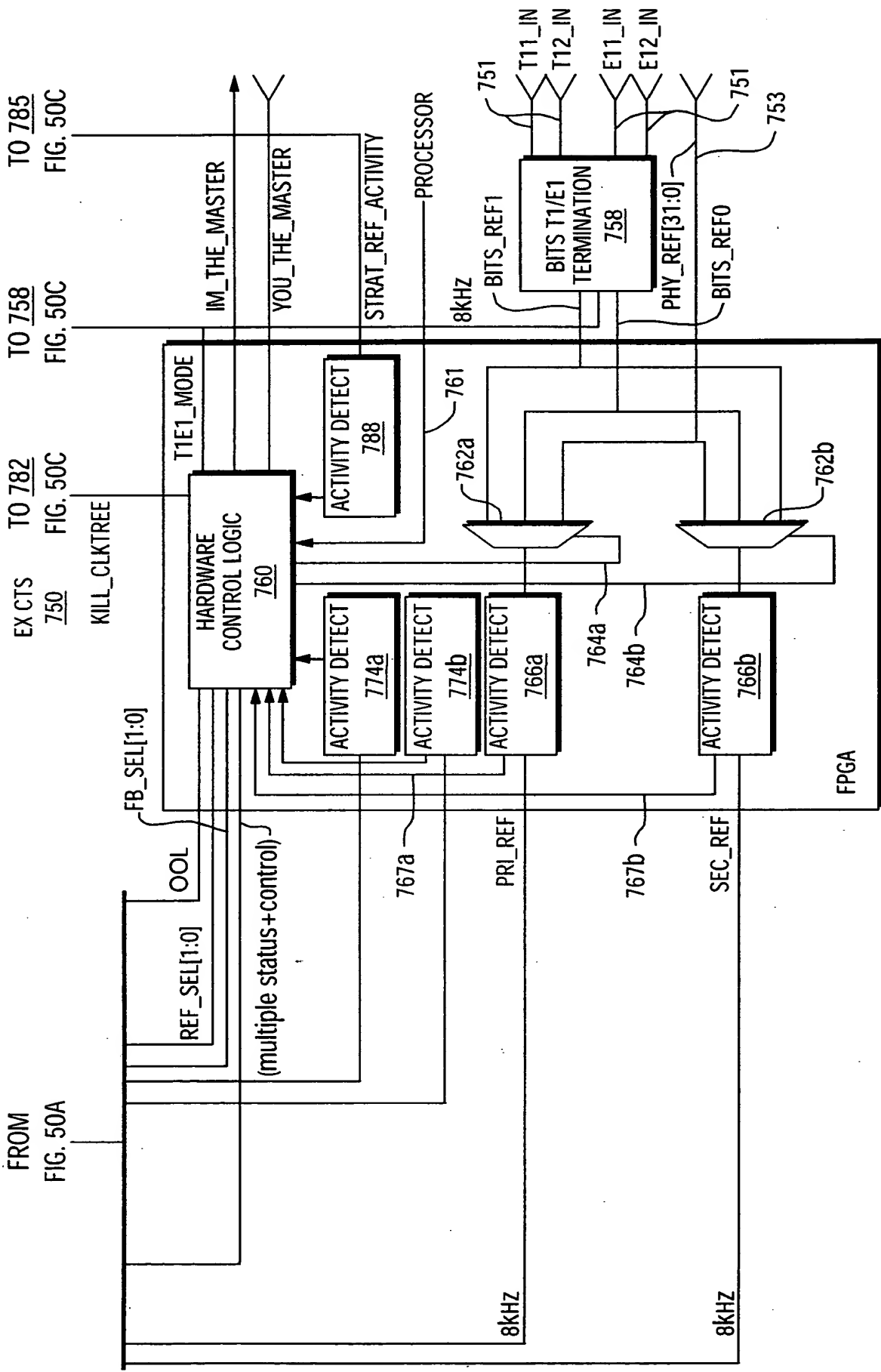


FIG. 50B

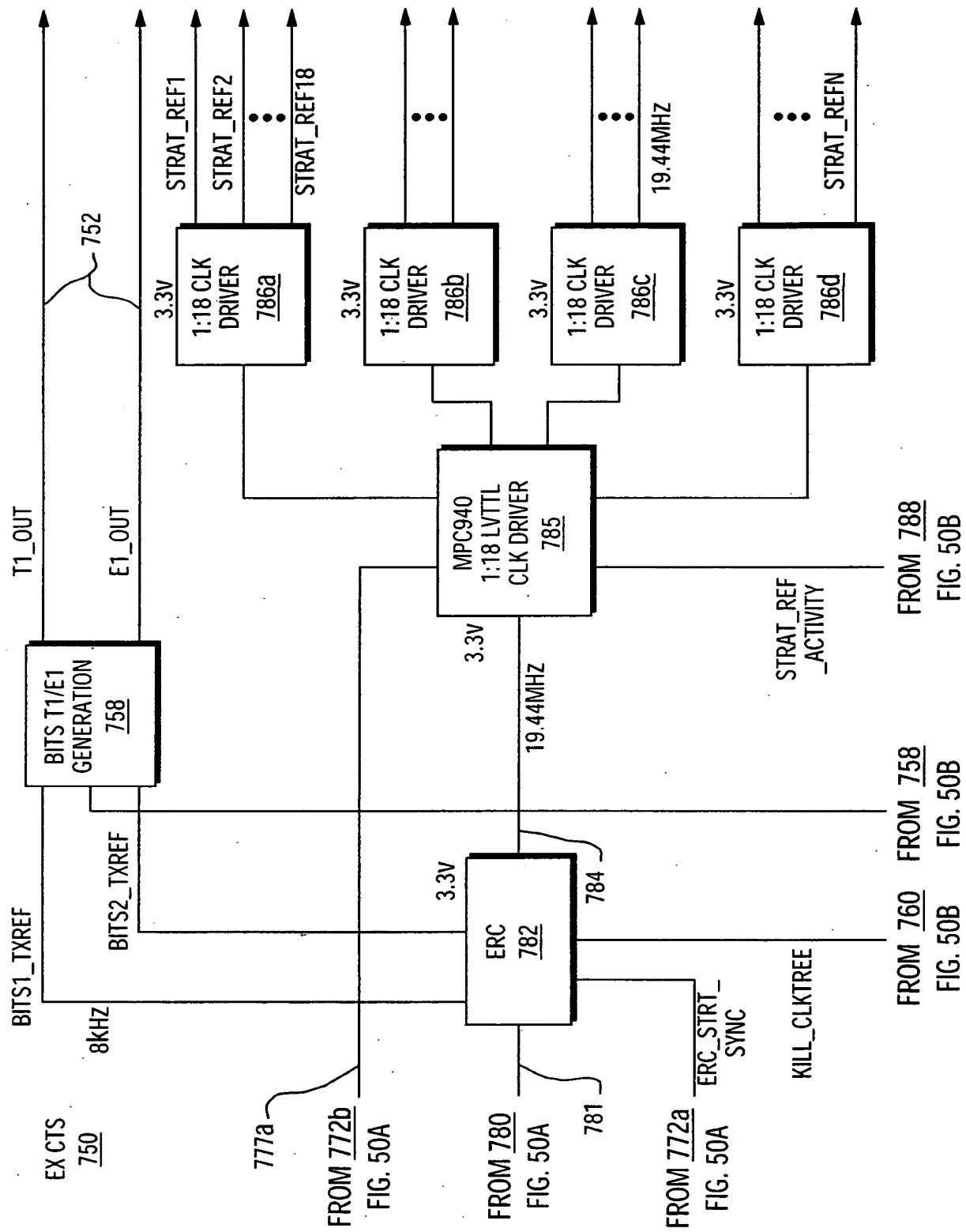


FIG. 50C

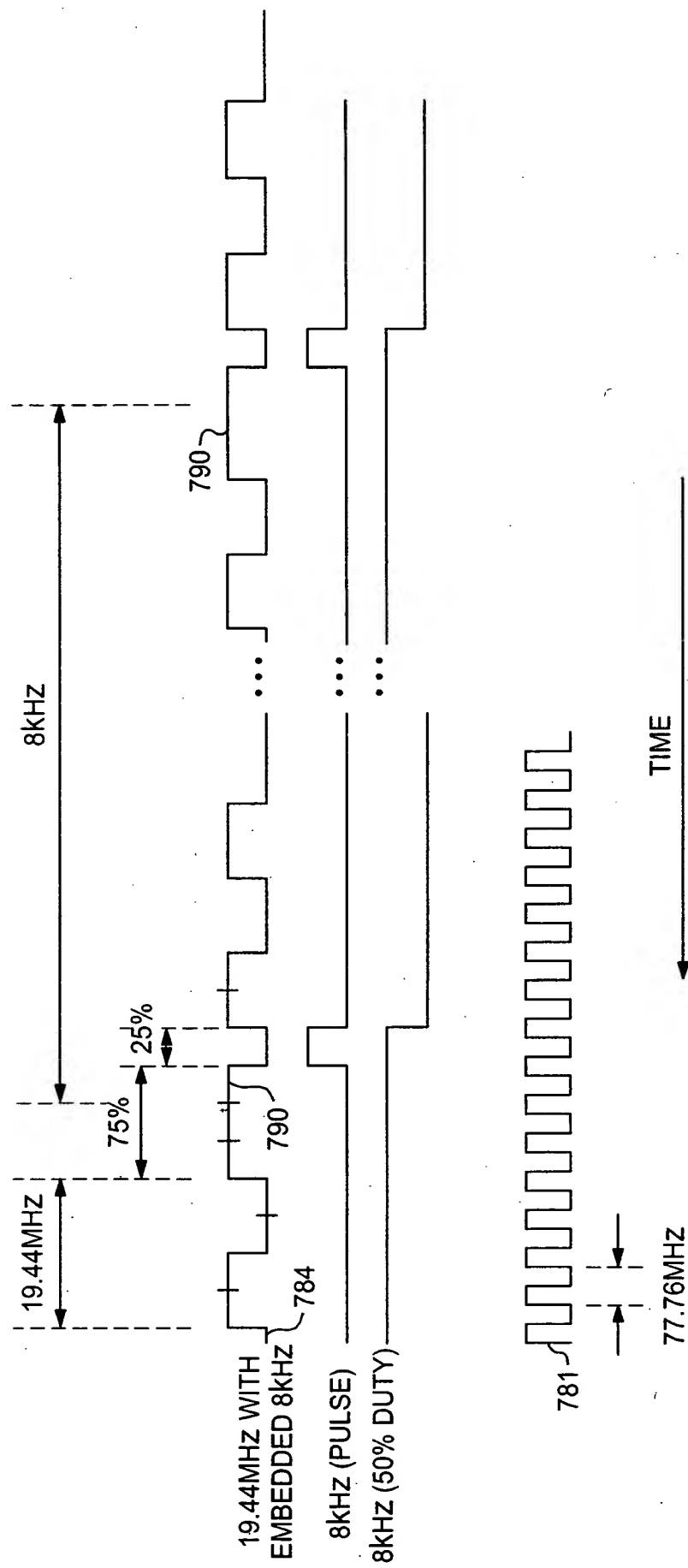


FIG. 51

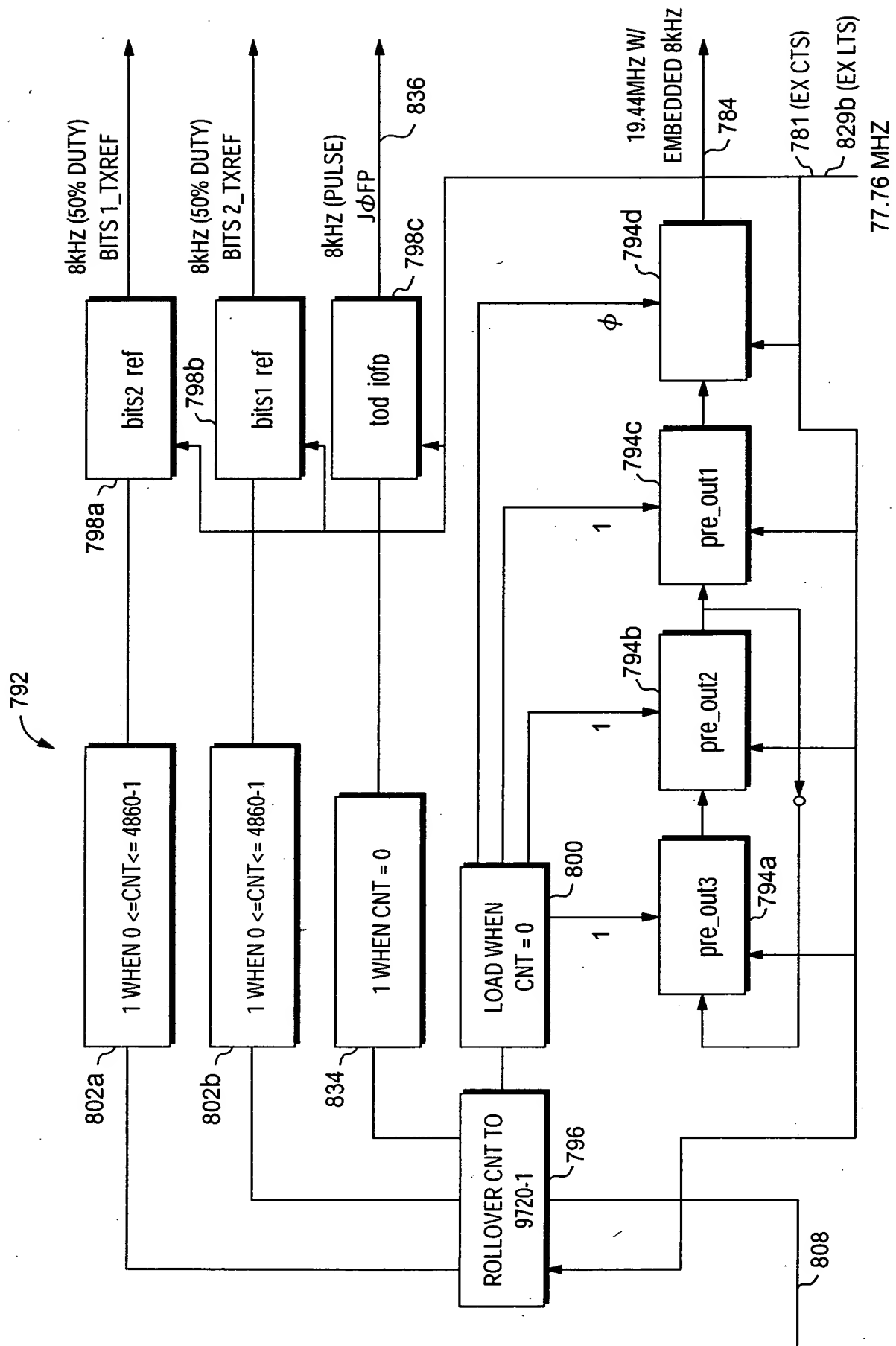


FIG. 52

EXTRACTOR
804

ERC_STRAT_SYNC (EX CTS)
STRAT_REF_A OR STRAT_REF_B (EX LTS) 832
19.44MHz WITH ENCLOSED 8kHz
(MUST BE PULLED LOW WHEN NOT PRESENT)

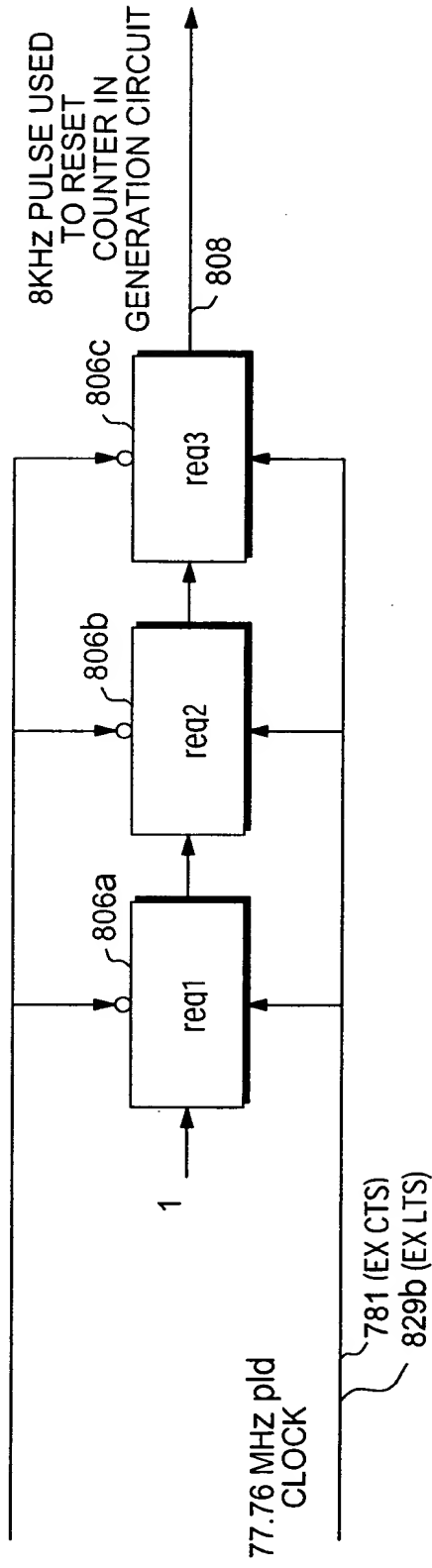
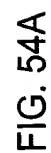


FIG. 53



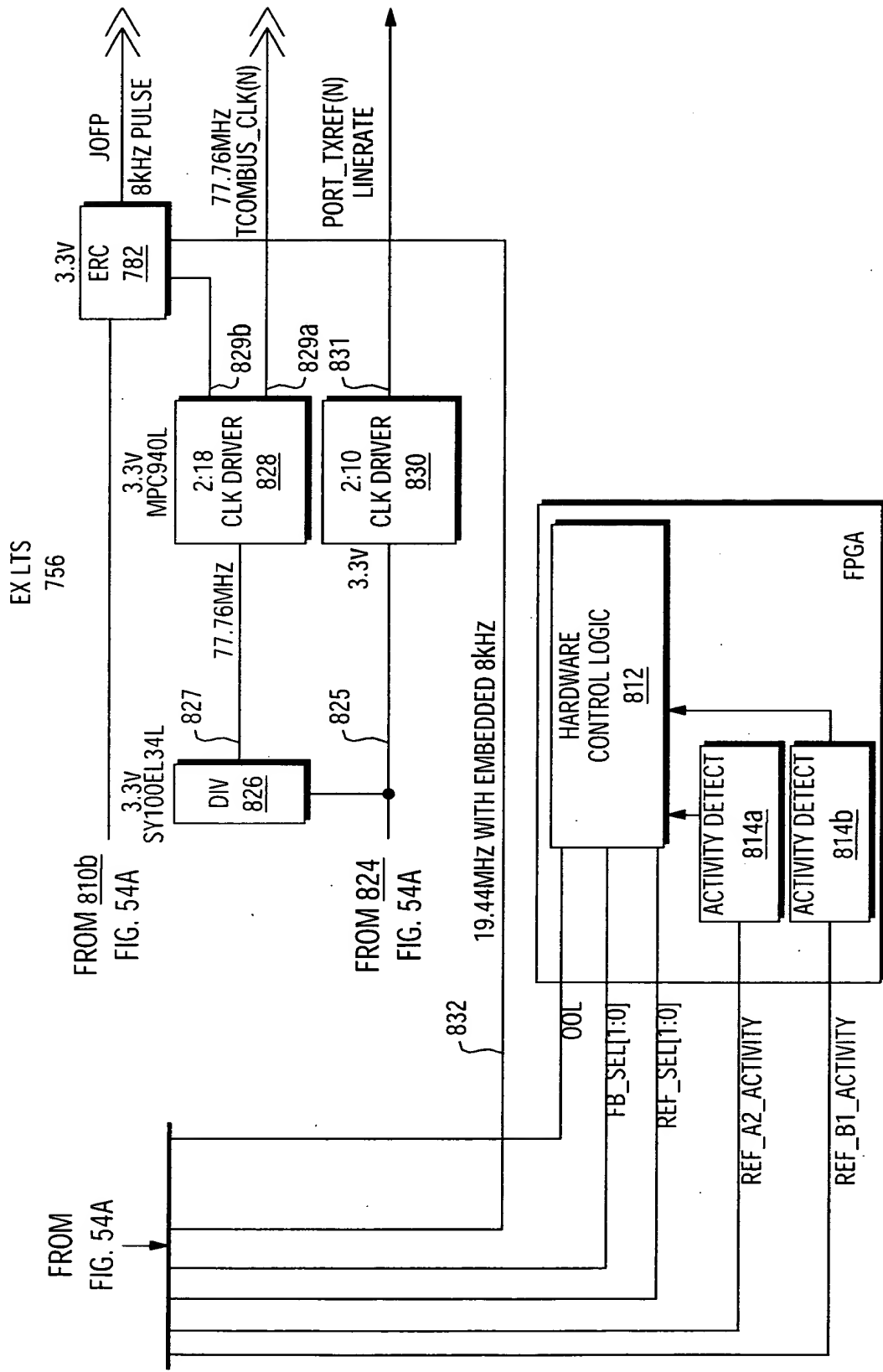


FIG. 54B

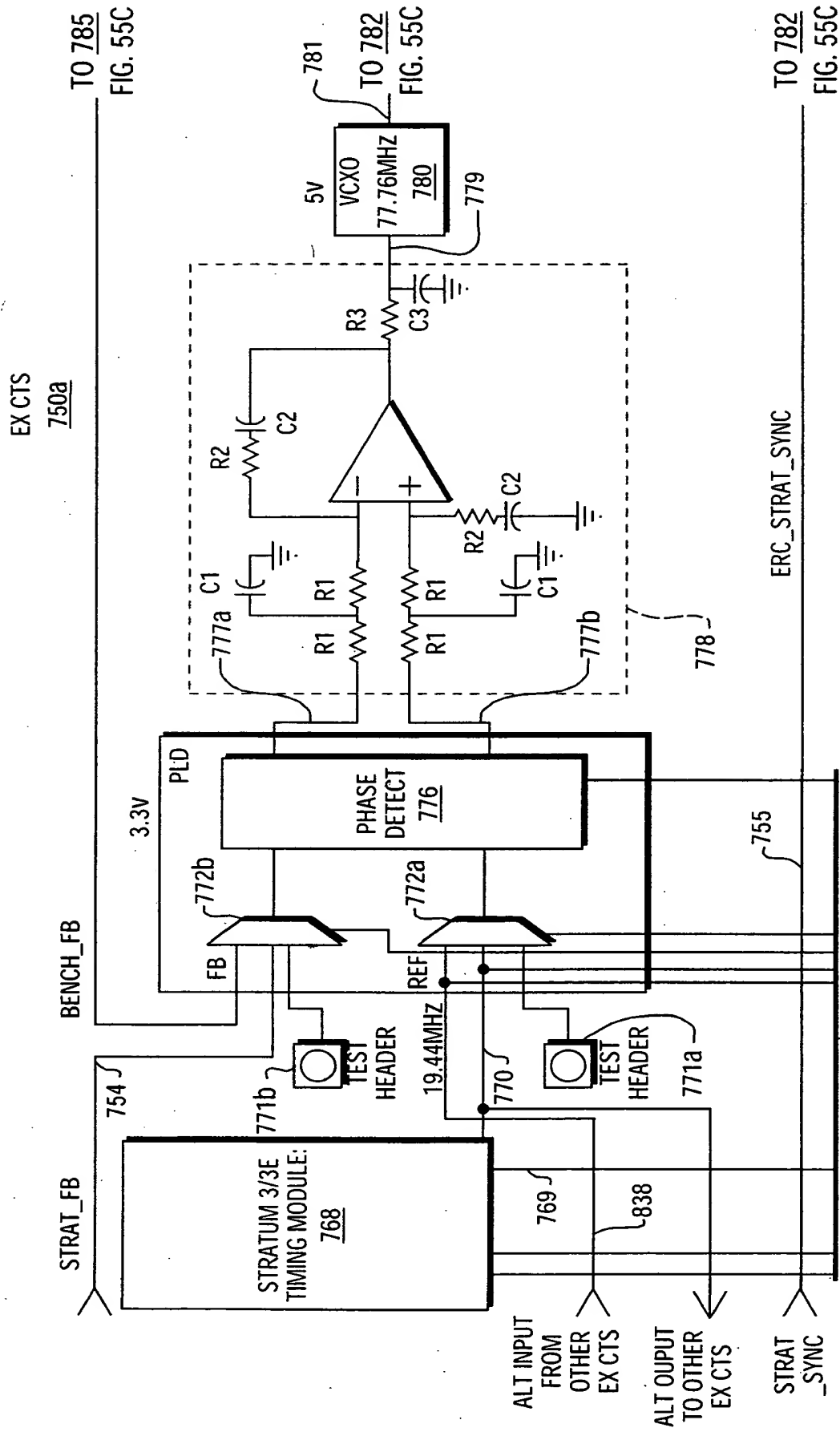


FIG. 55A

FIG. 55B

EX CTS
750a

FROM
FIG. 55A

TO 785
FIG. 55C

TO 758
FIG. 55C

TO 782
FIG. 55C

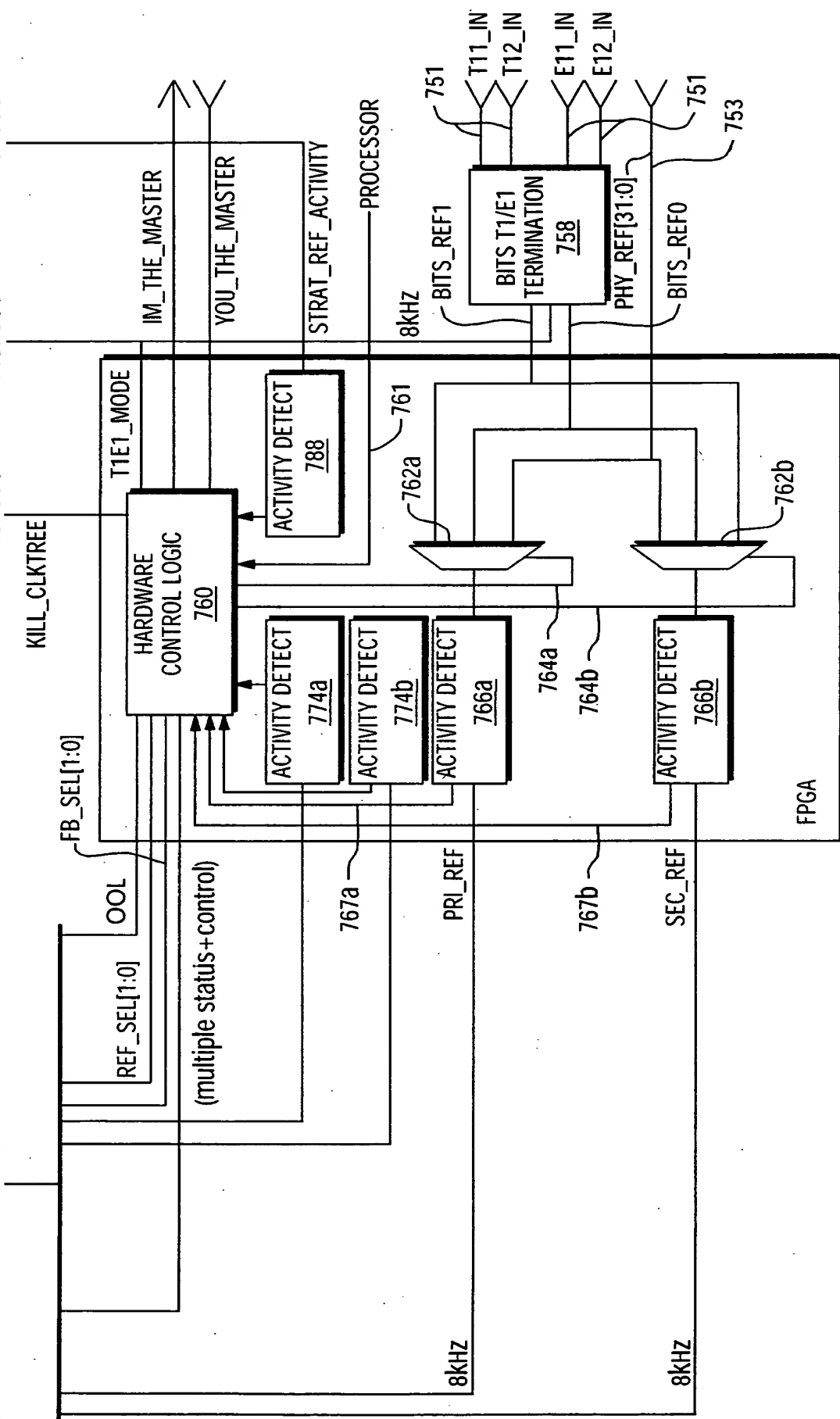


FIG. 55B

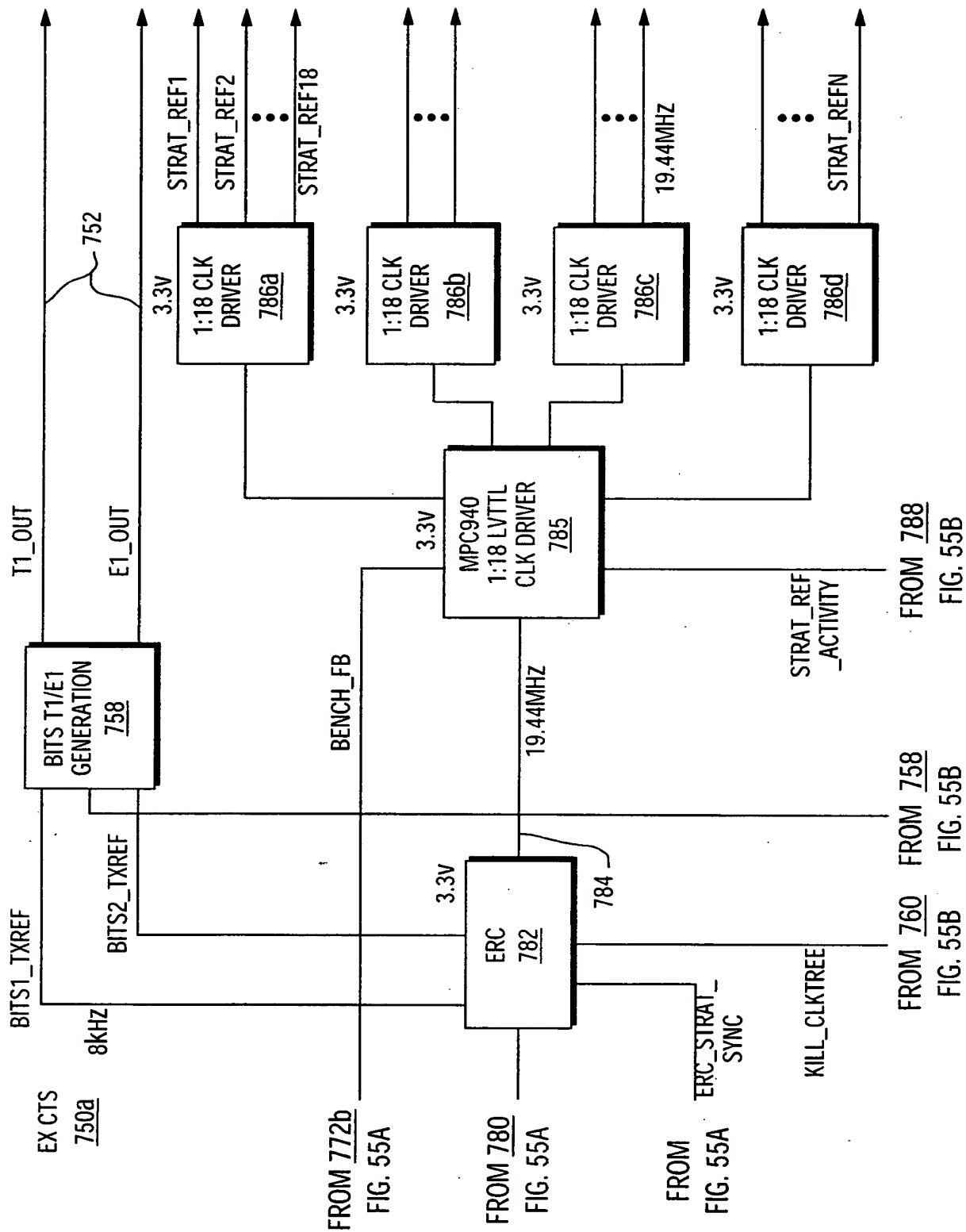


FIG. 55C